

MURI Review Meeting

November 12-13, 2003

Baltimore, Maryland

***** TENTATIVE *****

Agenda - Day 1

Wednesday, November 12

Static Analysis to Enhance the Power of Model Checking for Concurrent Software

0830-0900	OSD Introduction
0900-0930	<i>Assuring Software Quality</i> Ed Clarke
0930-0950	<i>Overview of UW Activities</i> Tom Reps
0950-1015	<i>Modular Verification of C Components</i> Sagar Chaki
1015-1040	<i>Shape Analysis</i> Tom Reps
1040-1055	BREAK
1055-1120	<i>Verification of Embedded Systems</i> Daniel Kroening
1120-1145	<i>Weighted Pushdown Systems</i> Tom Reps
1145-1215	<i>Wrap-Up</i> Ed Clarke and Tom Reps
1215-1315	LUNCH
1315-1415	<i>Poster Session</i> Various Students

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Agenda - Day 2

Thursday, November 13

Vulnerability and Information Flow Analysis of COTS

0830-0900	OSD Introduction
0900-0915	<i>WiSA Project Overview</i> Somesh Jha
0915-1000	<i>WiSA Architecture Overview and Applications</i> Somesh Jha
1000-1015	BREAK
1015-1100	<i>Static Analysis of x86 Binaries</i> Tom Reps
1100-1145	<i>Attacks and Defenses</i> Bart Miller
1145-1215	<i>Wrap-Up</i> Somesh Jha
1215-1315	LUNCH
1315-1415	<i>Demonstrations</i> Various Students
1415-1500	<i>Poster Session</i> Various Students