The Storage Hierarchy is Not a Hierarchy: Optimizing Caching on Modern Devices with Orthus

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Storage Hierarchy

• Long been central to system designs

"Ideally one would desire an indefinitely large memory capacity ... It does not seem possible to achieve such a capacity. We are therefore forced to recognize the possibility of constructing a hierarchy of memories ..."

---- "Preliminary Discussion of the Logical Design of an Electronic Computing Instrument" (1946), by Burks, Goldstine, and von Neumann.

Storage Hierarchy

- Simplified two-layer hierarchy
 - **Performance Device:** fast, expensive, small
 - Capacity Device: slow, cheap, large



Caching

Replicating popular items in **Performance Device**



Caching Wisdom: Maximizing Hit Rates

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- Caching delivers ~**Performance Device** speed along with **Capacity Device** capacity
- Traditionally, very good!
 - **Performance:** Performance Device >> Capacity device
 - E.g. DRAM vs. HDD (100x differences)



Problem: Caching is Insufficient in Modern Storage Hierarchies

• Insight:

the assumption (Performance device >> Capacity device) is broken

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- The differences between today's neighboring layers are less clear and even overlapping (depending on workloads)
- E.g., serving reads with high parallelism, Optane / SSD ~ Flash SSD, caching leaves huge / performance available in Flash SSD unexploited



It's imperative to rethink how modern hierarchies should be managed.

Our Approach: Non-Hierarchical Caching (NHC)

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• Key idea: augmenting caching with dynamic load **admission** and request **offloading**

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- Excess load examples:
 - Data admission to further improve hit rate
 - Too many cache hits



- Enable offloading: tunable caching behaviors
 - Classic caching is (data admit = true, load admit ratio = 100%) Ο

read hits

Load Switch

p >= load_admit

p < load_admit



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How much load to offload?

- **Observation**: different hierarchies, different workloads desire different split of load to devices (for best performance)
- Handle complexities: feedback-based cache scheduler

- feedback-based cache scheduler
 - Adjust tuning knobs (e.g., data_admit flag, load_admit ratio)



- feedback-based cache scheduler
 - Optimize a target performance metric
 - Target metric: user/device; throughput/ latency/ tail latency
 - **f(X):** a function to measure/compute the target metric



• State 1: begin with classic caching



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 - Ends when hit rate becomes "stable"



- Turn off data admission for read misses
- Start to tune load admit ratio (base point 100%)



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- End state 2 when: -> back to State 1 (classic caching)
 - Workload hit rate significantly changed
 - Find 100% load admit rate is always optimal



NHC - Key Properties

- Compatible with all classic caching implementations/ policies
- Require no prior knowledge of devices and workloads
- Robust to dynamic workloads

Implementation

• Implementation (Orthus):

- **Orthus-CAS:** block-layer caching kernel module, based on Intel Open CAS framework
- **Orthus-KV:** user-level caching layer for Wisckey [FAST' 16] (a LSM-tree based K/V store)

• Supported target metrics:

- Throught
- Avg. latency
- P99 latency

• Evaluated hierarchies:

- DRAM/Optane DC PM
- Optane DC PM/Optane SSD
- Optane SSD/Flash SSD







Normalized to cache device read bandwidth







Other Experiments in the Paper

- Orthus improves with various caching **policies**
- Orthus optimizes different target metrics (e.g., tail latency)
- Orthus improves YCSB workloads
- Orthus improves dynamic workloads, such as Facebook ZippyDB workloads [FAST' 20]
- ...

Conclusion

- Evolving storage hierarchies have strong implications for caching
 - Quantitative comparisons across modern storage devices
 - Characterizing caching performance in both classic and modern hierarchies
- Orthus optimizes classic caching, by dynamic load admission and request offloading
 - Is compatible with all classic caching policies
 - Requires no prior knowledge of devices and workloads
 - Adapts to dynamic workloads
 - Can improve performance (throughput, tail latency) by up to 2X over classic caching in various storage hierarchies, under a range of realistic workloads

Thank you & Questions?

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