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Abstract

Decentralized information flow control (DIFC) operating systems provide applications with mechanisms for enforcing information-flow policies for their data. However, significant obstacles keep such operating systems from achieving widespread adoption. One key obstacle is that DIFC operating systems provide only low-level mechanisms for allowing application programmers to enforce their desired policies. It can be difficult for the programmer to ensure that their use of these mechanisms enforces their high-level policies, while at the same time not breaking the underlying functionality of the application. These are issues both for programmers who would develop new applications for a DIFC operating system and for programmers who would port existing applications to a DIFC operating system.

Our work significantly eases this task. We present an automatic technique that takes as input a program with no DIFC code, and two policies: one that specifies prohibited information flows and one that specifies flows that must be allowed. Our technique then produces a new version of the input program that satisfies the two policies. To evaluate our technique, we created an automatic tool, called SWIM (for **Secure What I Mean**), that implements the technique, and applied it to a set of real-world programs and policies. The results of our evaluation demonstrate that the technique is both sufficiently expressive to generate code for real-world policies, and that it can generate such code efficiently. It thus represents a significant contribution towards developing systems with strong end-to-end information-flow guarantees.

1. Introduction

Decentralized information flow control (DIFC) operating systems provide applications with mechanisms for ensuring the secrecy and integrity of their data [12, 19, 21] throughout a system. To enable this, a DIFC OS maps the set of executing processes to a partially-ordered set of *labels*. A process may send data to another process only if the processes' labels satisfy a certain ordering. A process may change its own labels and the labels of other processes, under restrictions enforced by the DIFC OS. Thus processes express how they intend their information to be shared by associating the labels with information, and the DIFC OS respects these intentions by enforcing a semantics of labels.

Previous work has concerned how to implement DIFC systems, in some cases atop standard operating systems. Furthermore, some systems have formal proofs that if an application running on the system correctly manipulates labels to implement a policy, then the system will enforce the policy [11]. However, for a user to have end-to-end assurance that their application implements a high-level information-flow policy, they must have assurance that the application indeed correctly manipulates labels. The label manipulations allowed by DIFC systems are expressive but low-level, so a high-level policy is semantically distant from a program's label manipulations. For the remainder of this paper, we narrow our discussion from general DIFC systems to Flume [12]. In principle, our approach can be applied to arbitrary DIFC operating systems. However, targeting Flume yields both theoretical and practical benefits.

```
void ap_mpm_run()
A1: while (*)
A2:   Conn c = get_request_connection();
A3:   Conn c' = c; c = fresh_connection();
A4:   tag_t t = create_tag();
A5:   spawn('proxy', {c, c'}, {t}, {t}, {t});
A6:   spawn('proxy', {c', c'}, {t}, {t}, {t});
A7:   spawn('worker', c, {t}, {t}, {});

void proxy(Conn c, Conn c',
           Label lab, Label pos_cap, Label neg_cap)
P1: while (*)
P2:   expand_label(pos_cap);
P3:   Buffer b = read(c);
P4:   clear_label(neg_cap);
P5:   write(c', b);
```

Figure 1. An example derived from the Apache multi-process module.

From a theoretical standpoint, Flume defines the semantics of manipulating labels in terms of set operations, a well-understood formalism. From a practical standpoint, Flume runs on Linux, giving our approach wide applicability. The work in [12] gives a comprehensive description of the Flume system.

To illustrate the gap between low-level label manipulations and high-level policies, consider the server program in Fig. 1, a simplified excerpt from an Apache multi-process module (MPM) [1]. For now, suppose that the program code consists only of the non-highlighted code. In this program, an MPM process executes the function `ap_mpm_run`, iterating through the loop indefinitely (lines A1 - A7). On each iteration of the loop, the MPM waits for a new connection `c` that communicates information for a service request (line A2). When the MPM receives a connection, it spawns a *Worker* process to handle the request (line A7). One desirable property for such a server is to isolate each *Worker* process: no *Worker* process should be able to leak information to another *Worker* process, even if both processes are compromised and acting in collusion. It is difficult to design a server that upholds such a property: a *Worker* process may be compromised through any of a myriad of vulnerabilities, such as a stack-smashing attack. Once processes are compromised, they can communicate through any of several channels on the system, such as the file system.

However, the server can isolate *Worker* processes when executed on Flume. Suppose that the server in Fig. 1 is rewritten to include the highlighted code, which makes use of an API provided by the Flume. At a high level, Flume maps each process to three sets of *tags*, or atomic elements: the process's secrecy *label*, its *positive*

* Also affiliated with GrammaTech, Inc.

capability, and its *negative capability*.¹ A process may only alter its label by adding to it tags in its positive capability, or removing from it tags in its negative capability. In the highlighted code, the server uses the Flume API to ensure process isolation as follows. As before, an MPM process executes the function `ap_mpm_run`, and iterates through the loop indefinitely. Each time through the loop, it waits for a connection to a service request. Now, however, before spawning a Worker to handle the request, the MPM process performs two additional tasks. At line A4, it creates a fresh tag, and then initializes the label of the next Worker process with the tag, but does not give the Worker the capability to add or remove the tag from its label (line A7). This is the key to isolating the Worker processes. The Flume reference monitor intercepts all communications between processes, and only allows a communication to succeed if the label of the sending process is a subset of the label of the receiving process. Because the label of each Worker now contains a distinct tag that the Worker cannot remove, no Worker can send data to another. Flume associates other system objects, such as files, with tags as well, so the processes cannot communicate through files either.

However, while the label mechanisms provided by Flume are powerful, their power can lead to unintended side effects. In Fig. 1, the MPM process may not be able to alter the label of a process that issues service requests. If the MPM gave each Worker a unique tag and manipulated labels in no other way, then the label of each Worker would contain a tag t , while the label of the process that issued the request would not contain t . Thus the label of the Worker would not be a subset of the label of the receiver, and the Worker could not send information to the Requester. To resolve this, along with each Worker, the MPM process spawns two processes to serve as proxies for when the Worker receives information from (line A5) and sends information to (line A6) the Requester. Let $Proxy_s$ be the process forwarding information from its associated Worker to the service requester. The MPM gives to $Proxy_s$ the capability to add and remove from its label the tag t . To receive data from the Worker, $Proxy_s$ expands its label to include t ; to forward the data to the requester, it clears its label to be empty. Thus the untrusted Worker is isolated, while the small and trusted Proxy successfully communicates information between Worker and Requester.

This example illustrates that labels are a powerful mechanism for enabling application programmers to enforce information-flow policies. However, the example also illustrates that there is a significant gap between the high-level policies of programmers, e.g., that no Worker should be able to send information to another Worker, and the manipulations of labels required to implement such a policy. It also illustrates that these manipulations may be quite subtle when balancing desired security goals with the required functionality of an application. If programmers are to develop applications for DIFC systems, then they must resolve these issues manually. If they wish to instrument existing applications to run on DIFC systems, then they must ensure that their instrumentation implements their policy while not breaking the functionality requirements of an existing, and potentially large and complex, program. If their instrumentations do break functionality, it can be extremely difficult to discover this through testing: when a DIFC system blocks a communication, it may not report the failure, because such a report can leak information [12]. Label-manipulation code could thus introduce a new class of security and functionality bugs that could prove extremely difficult even to observe.

We have addressed this problem by creating an automatic *DIFC instrumenter*, which produces programs that, by construction, sat-

¹ Flume also allows processes to protect their integrity via an integrity label. In this work, we focus on secrecy, but our techniques can be extended to reason about integrity. See App. D.

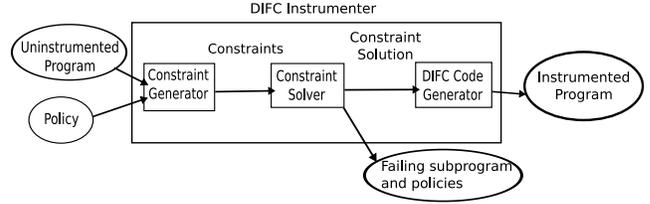


Figure 2. Workflow of the DIFC instrumenter.

isfy a high-level DIFC policy. Our DIFC instrumenter takes as input a program with no DIFC code, and two high-level declarative policies: one that specifies prohibited information flows (e.g. “Workers should not communicate with each other.”), and one that specifies flows that must be allowed (e.g. “A Worker should be able to communicate to a proxy.”). When successful, the instrumenter rewrites the input program with label-manipulation code so that it enforces the input policy. When unsuccessful, the instrumenter produces a minimal subprogram of the original program and a minimal subset of policies for which it could not find an instrumentation. To do so, the instrumenter reduces the problem of correctly instrumenting the program to a problem of solving a system of set constraints. It feeds the resulting constraint system to an off-the-shelf Satisfiability Modulo Theories (SMT) solver [7], which in our experiments found solutions in seconds (cf. Tab. 4). From a solution, the instrumenter instruments the program. Thus the programmer reasons at the policy level about information flow, and leaves to the instrumenter the task of correctly manipulating labels. If the programmer provides as input to the instrumenter the program in Fig. 1 minus the statements that manipulate labels, and a formal statement of a high-level policy similar to the one stated above, the instrumenter produces the entire program given in Fig. 1.

The remainder of this paper is organized as follows: §2 gives an overview of our technique by describing the steps that it takes to instrument the example in Fig. 1. §3 formally describes the technique. §4 reports our experience applying the technique to real-world programs and information-flow policies. §5 places our work in the context of other work on DIFC systems and program synthesis. §6 concludes. Some technical details are covered in the Appendix.

2. Overview

We now informally describe each step of the workflow of the DIFC instrumenter using the example from Fig. 1. Fig. 2 illustrates the workflow.

2.1 Programs and Policies as Inputs

The instrumenter takes two inputs. First, it takes a program containing no Flume code. For the example in Fig. 1, the instrumenter takes the version of the server uninstrumented with calls to `create_tag()`, `expand_label()`, and `clear_label()`. The instrumenter analyzes programs represented as Communicating Sequential Processes (CSP) [2], but can automatically translate a program written in an imperative programming language, such as C, into a CSP program that models properties relevant for DIFC instrumentation.

Exa. 1. *The instrumenter translates the Apache server introduced in Fig. 1 to the following CSP program:*

$$\begin{array}{ll}
 A_1 = A_5 & P_1 = P_3 \\
 A_5 = A_6 \parallel P_1 & P_3 = ?r \rightarrow P_5 \\
 A_6 = A_7 \parallel P_1 & P_5 = !s \rightarrow P_1 \\
 A_7 = A_1 \parallel W & \text{init} = A_1 \parallel R
 \end{array}$$

In the equations of the CSP program, each variable corresponds to a program point in the imperative program, and each equation defines the behavior of the program at the corresponding point. The expressions on the right-hand sides of equations are referred to as **CSP process templates**. The equation $A_5 = A_6 \parallel P_1$ denotes that a process executing A_5 transitions to launch two processes, one executing A_6 and the other executing P_1 . The equation $P_3 = ?r \rightarrow P_5$ denotes that a process executing P_3 receives a message from process r and then transitions to P_5 . The requester process executes template R . The definitions of other process template variables are similar. §3.1 contains a more comprehensive discussion of CSP.

Second, the instrumenter takes as input a policy that specifies correct information flow. Policies are sets of declarative *flow assertions*, which are of two forms. A flow assertion of the form $\text{Secrecy}(\text{Source}, \text{Sink}, \text{Declass}, \text{Anc})$ specifies that any process executing template Source must not be able to send information to a process executing template Sink unless the information flows through a process executing template Declass , or the Source and Sink processes were transitively created by the *same process* executing template Anc . A flow assertion of the form $\text{Prot}(\text{Source}, \text{Sink}, \text{Anc})$ specifies that if a process executing template Source attempts to send information to a process executing template Sink , and both the Source and Sink processes were transitively created by the same process executing template Anc , then the send must be successful.

Exa. 2. The information-flow policy for the server in Fig. 1 can be expressed as the set of flow assertions: $\{\text{Secrecy}(W, W, \{P_1, P_2, P_3\}, W), \text{Prot}(W, P_2, R), \text{Prot}(P_3, R, \text{init})\}$, where init is a special process template that spawns all process templates.

In addition to flow assertions, the instrumenter takes a set of rules declaring which process templates denote processes that may be compromised. In Fig. 1, the instrumenter takes a rule declaring that the Worker may be compromised.

2.2 From Programs and Policies to Instrumentation Constraints

Given a program and policy, the instrumenter generates a system of set constraints such that a solution to the constraints corresponds to an instrumentation of the program that satisfies the policy. This constraint system must assert that (1) the instrumented program uses the Flume API to manipulate labels in a manner allowed by Flume, and (2) the instrumented program manipulates labels to satisfy all flow assertions.

Each variable in the constraint system corresponds to a label value for the set of all processes that execute a given CSP template. Flume restricts how a process manipulates its label in terms of the capabilities of the process. The instrumenter expresses this in the constraint system by generating, for each CSP process template P , variables that represent the set of tags in the label of a process executing P (lab_P), its positive capability (pos_P), its negative capability (neg_P), and the set of tags created when executing P (creates_P).

Some constraints in the system assert that each process’s labels may only change in ways allowed by Flume. These constraints assert that in each step of execution, a process’s label may grow no larger than is allowed by its positive capability, and may shrink no smaller than is allowed by its negative capability.

Exa. 3. To model how the label and capabilities of a process may change in transitioning from executing template A_1 to template A_5 ,

the instrumenter generates the following four constraints:

$$\begin{aligned} \text{lab}_{A_5} &\subseteq \text{lab}_{A_1} \cup \text{pos}_{A_5} \\ \text{lab}_{A_5} &\supseteq \text{lab}_{A_1} - \text{neg}_{A_5} \\ \text{pos}_{A_5} &\subseteq \text{pos}_{A_1} \cup \text{creates}_{A_5} \\ \text{neg}_{A_5} &\subseteq \text{neg}_{A_1} \cup \text{creates}_{A_5} \end{aligned}$$

The other constraints in the system assert that the instrumented program does not allow flows that are specified by a Secrecy flow assertion, but allows all flows specified in a Prot assertion.

Exa. 4. For the server in Fig. 1, the flow assertion $\text{Secrecy}(W, W, \{P_1, P_3, P_5\}, W)$ specifies that a Worker process, which executes template W , may not send information to a different Worker process unless the workers are created by the same MPM process that executed template A_1 , or the information flows through a process that executes a Proxy template P_1 , P_3 , or P_5 . In §3.3.2, we define a set Dist_{A_1} of process templates, which the instrumenter uses to encode the flow assertion as

$$\text{lab}_W \not\subseteq \left(\text{lab}_W - \bigcup_{R \in \text{Dist}_{A_1}} \text{creates}_R \right) \cup \bigcup_{Q \notin \{P_1, P_3, P_5\}} \text{neg}_Q$$

For illustrative purposes, this constraint is slightly simplified from its complete form, and may not lead the instrumenter to isolate Worker processes that are compromised. However, the constraint can be extended to do so in a straightforward way using techniques described in App. C.

Exa. 5. For the server in Fig. 1, the flow assertion $\text{Prot}(W, P_3, A_1)$ specifies that a Worker process executing template W must be able to send information to a process executing Proxy template P_3 if the two processes were created by the same MPM process executing template A_1 . In §3.3.2, we define a set Const_{A_1} of process templates, which the instrumenter uses to encode the flow assertion as

$$\text{lab}_W \subseteq \text{lab}_{P_3} \cap \bigcup_{Q \in \text{Const}_{A_1}} \text{creates}_Q$$

A similar flow assertion $\text{Prot}(P_5, R, \text{init})$ specifies that a Proxy process executing P_5 must always be able to send data to the Requester executing R . The instrumenter encodes this assertion as

$$\text{lab}_{P_5} \subseteq \text{lab}_R \cap \bigcup_{Q \in \text{Const}_{\text{init}}} \text{creates}_Q$$

The constraints generated for the example from Fig. 1 are given in Tab. 1. The table contains a representative sample of the semantic constraints; analogous constraints are generated for the other process templates in the program.

2.3 From Instrumentation Constraints to DIFC Code

Solving the constraint systems described in §2.2 is an NP-complete problem. Intuitively, the complexity arises because such a constraint system may contain both positive and negative subset constraints and union set expressions, as shown in Tab. 1. See [8, App. G] for a formal proof. However, we can show that if the constraint system has a solution, then it has a solution in which all variables have a value with no more than N tags, where N is the number of Secrecy assertions in the policy. Using the bound N , the instrumenter translates the system of set constraints to a system of bit-vector constraints such that the set-constraint system has a solution if and only if the bit-vector system has a solution. Bit-vector constraints can be solved efficiently in practice by an off-the-shelf SMT solver [7]. The translator thus feeds the bit-vector system to such a solver; if the solver determines that no solution exists for

Semantics		Secrecy	Protected Flows
$\text{lab}_{A_5} \subseteq \text{lab}_{A_1} \cup \text{pos}_{A_5}$ $\text{lab}_{A_5} \supseteq \text{lab}_{A_1} - \text{neg}_{A_5}$ $\text{pos}_{A_5} \supseteq \text{pos}_{A_1} \cup \text{creates}_{A_5}$ $\text{neg}_{A_5} \supseteq \text{neg}_{A_1} \cup \text{creates}_{A_5}$...	$\text{lab}_W \not\subseteq \left(\text{lab}_W - \bigcup_{R \in \text{Dist}_{A_1}} \text{creates}_R \right) \cup \bigcup_{Q \notin \{P_1, P_3, P_5\}} \text{neg}_Q$	$\text{lab}_W \subseteq \text{lab}_{P_3} \cap \bigcup_{Q \in \text{Const}_{A_5}} \text{creates}_Q$ $\text{lab}_{P_5} \subseteq \text{lab}_R \cap \bigcup_{Q \in \text{Const}_{\text{init}}} \text{creates}_Q$	

Table 1. A representative selection of constraints for the Apache server.

the bit-vector constraints, then it produces an *unsatisfiable core*, which is a minimal set of constraints that are unsatisfiable. The instrumenter determines the subprogram of the original program and subset of flow assertions that contributed the constraints in the infeasible core. It reports to the user that it may not be possible to instrument the program to satisfy the policy, and as a programming aid, provides the subprogram and policies that contributed the infeasible core. On the other hand, if the SMT solver finds a solution to the bit-vector constraints, then the instrumenter translates this to a solution for the system of set constraints.

Using the solution to the set-constraint system, the instrumenter injects into the original program DIFC code that defines the label values of all processes over any execution of the program to correspond to the values in the constraint solution. By construction, the resulting program satisfies the given information-flow policy.

Exa. 6. *One Secrecy flow assertion contributes to the constraint system summarized in Tab. 1. The instrumenter thus determines that if the system has a solution, then it has a solution that can be defined over a set of one element. Using this bound, the instrumenter translates the system to an equisatisfiable bit-vector system, and feeds the bit-vector system to an SMT solver. The solver determines that the bit-vector system has a solution corresponding to the following set-valued solution defined over the set of elements $\{\tau\}$:*

X	lab_X	pos_X	neg_X	creates_X
A_1	\emptyset	\emptyset	\emptyset	\emptyset
A_5	\emptyset	$\{\tau\}$	$\{\tau\}$	$\{\tau\}$
A_6	\emptyset	$\{\tau\}$	$\{\tau\}$	\emptyset
A_7	\emptyset	$\{\tau\}$	$\{\tau\}$	\emptyset
P_1	$\{\tau\}$	$\{\tau\}$	$\{\tau\}$	\emptyset
P_3	$\{\tau\}$	$\{\tau\}$	$\{\tau\}$	\emptyset
P_5	\emptyset	$\{\tau\}$	$\{\tau\}$	\emptyset
W	$\{\tau\}$	\emptyset	\emptyset	\emptyset
R	\emptyset	\emptyset	\emptyset	\emptyset

The instrumenter uses the solution to generate the DIFC code highlighted in Fig. 1. In the solution, $\text{creates}_{A_5} = \{\tau\}$, so the instrumenter inserts at line A_5 a call to create a tag t . In the solution, $\text{lab}_{P_1} = \text{pos}_{P_1} = \text{neg}_{P_1} = \{\tau\}$, so the instrumenter rewrites spawns of Proxy processes so that all Proxy processes are initialized with t in their label, positive capability, and negative capability. In the solution, $\text{lab}_{P_3} = \{\tau\}$ while $\text{lab}_{P_5} = \emptyset$, so the instrumenter inserts at P_5 code that clears t from the label of the process. The final result is the full code given in Fig. 1.

3. DIFC Instrumentation

We now discuss the DIFC instrumenter in more detail. We first formally describe the programs and policies that the instrumenter takes as input, and then describe each of the steps it takes to instrument a program.

3.1 DIFC Programs

The instrumenter analyzes programs in a variation of CSP that we call CSP_{DIFC} . Imperative programs are translated automatically to CSP_{DIFC} programs using a straightforward translation method

$\text{Prog} := \text{PROCVAR}_1 = \text{Proc}_1 \dots \text{PROCVAR}_n = \text{Proc}_n$

$\text{Proc} := \text{SKIP}$

| PROCVAR

| $\text{EVENT} \rightarrow \text{PROCVAR}$

| $\text{PROCVAR}_1 \square \text{PROCVAR}_2$

| $\text{PROCVAR}_1 \parallel \text{PROCVAR}_2$

$\text{EVENT} := \text{ChangeLabel}(\text{LABEL}, \text{LABEL}, \text{LABEL})$

| CREATE_τ

| ? PROC_ID

| ! PROC_ID

Figure 3. CSP_{DIFC} : a fragment of CSP used to model the behavior of DIFC programs. Events in gray are not contained in programs provided by the user. They are only generated by the DIFC instrumenter.

spelled out in [8, App. I]. The syntax of CSP_{DIFC} is given in Fig. 3. A CSP_{DIFC} program \vec{P} is a set of equations, each of which binds a *process template* to a process-template variable. Intuitively, a process template is the “code” that a process may execute. For convenience, we sometimes treat \vec{P} as a function from template variables to the templates to which they are bound.

The semantics of CSP_{DIFC} follows that of standard CSP [2], but is extended to handle labels. The state of a CSP_{DIFC} program is a set of processes. Processes are scheduled non-deterministically to execute their next step of execution. The program state binds each process to:

1. A process template, which defines the effect on the program state of executing the next step of the process.
2. A label, positive capability, and negative capability, which constrain how information flows to and from the process.
3. A namespace of tags, which constrain what tags the process may manipulate.

We give CSP_{DIFC} a trace semantics, which associates to every CSP_{DIFC} program \vec{P} the set of traces of *events* that \vec{P} may generate over its execution. Events consist of:

1. One process taking a step of execution.
2. One process spawning another process.
3. One process sending information to another.
4. One process receiving information from another.

Whenever a process p bound to template variable X takes a step of execution, p generates an event $\text{STEP}(X)$. p then spawns a fresh process p' , generates an event $\text{SPAWNS}(p, p')$, sets the labels of p' to its own label values, sets the tag namespace of p' equal to its own, and halts. However, when $\vec{P}(X) = \text{ChangeLabel}(L, M, N) \rightarrow \text{PROCVAR}_1$ or $\vec{P}(X) = \text{CREATE}_\tau \rightarrow \text{PROCVAR}_1$, no events are generated in the trace. This allows us to state desired properties of an instrumentation naturally using equality over traces (see §3.2.2). Note that this definition of \vec{P} is purely conceptual: pro-

grams produced by the instrumenter do not generate fresh processes at each step of execution.

When a process p takes a step of execution, it may have further effects on the program state and event trace. These effects are determined by the template to which p is bound. The effects are as follows, according to the form of the template:

- SKIP: p halts execution.
- PROCVAR: p initializes a fresh process p' to execute the template \vec{P} (PROCVAR).
- PROCVAR₁ □ PROCVAR₂: p chooses non-deterministically to initialize p' to execute either template PROCVAR₁ or template PROCVAR₂.
- PROCVAR₁ ||| PROCVAR₂: p spawns a fresh process p' , which it initializes to execute template PROCVAR₁, and a second fresh process p'' , which it initializes to execute template PROCVAR₂.
- CREATE _{τ} → PROCVAR₁: p creates a new tag t , binds it to the tag identifier τ in the tag namespace of p , and adds t to both the positive and negative capabilities of p' . Tag t is never bound to another identifier, so at most one tag created at a given CREATE template can ever be bound in the namespace of a process. However, multiple tags created at a template can be bound in the namespaces of multiple processes.
- ChangeLabel(L, M, N) → PROCVAR₁: L, M , and N are sets of tag identifiers. p initializes p' to execute PROCVAR₁, and attempts to initialize the label, positive capability, and negative capability of p' to the tags bound in the namespace of p to the identifiers in L, M , and N , respectively. Each initialization is only allowed if it satisfies the conditions enforced by Flume: (1) the label of p' may be no larger (smaller) than the union (difference) of the label of p and the positive (negative) capability of p , and (2) the positive (negative) capability of p' may be no larger than the union of the positive (negative) capability of p and capabilities for all tags created at p' .
- ! q → PROCVAR₁: p attempts to send information to process q . For simplicity, we assume that a process may attempt to send information to any process, and make a similar assumption for when p attempts to receive information. p generates an event $p!q$ only if it successfully sends information; that is, the label of p is contained in the label of q . Process p then initializes p' to execute template PROCVAR₁.
- ? q → PROCVAR₁: p attempts to receive information from q . p generates an event $p?q$ only if it successfully receives information; that is, the label of p contains the label of q . Process p then initializes p' to execute template PROCVAR₁.

$\text{Tr}(\vec{P})$ denotes the set of all traces of events that program \vec{P} may generate. A formal definition of $\text{Tr}(\vec{P})$ is given in [8, App. E].

3.2 DIFC Policies

Policies give a formal condition for when one program is a correct instrumentation of another.

3.2.1 Syntax of DIFC Policies

A DIFC policy $\mathcal{F} = (\mathcal{V}, \mathcal{S}, \mathcal{R})$ contains two sets, \mathcal{S} and \mathcal{R} , of flow assertions defined over a set \mathcal{V} of template variables. \mathcal{S} is a set of *secrecy assertions*, each of the form $\text{Secrecy}(\text{Source}, \text{Sink}, \text{Declass}, \text{Anc})$, with $\text{Source}, \text{Sink}, \text{Anc} \in \mathcal{V}$ and $\text{Declass} \subseteq \mathcal{V}$. \mathcal{R} is a set of *protection assertions*, each of the form $\text{Prot}(\text{Source}, \text{Sink}, \text{Anc})$, with $\text{Source}, \text{Sink}, \text{Anc} \in \mathcal{V}$.

3.2.2 Semantics of DIFC Policies

The semantics of a policy $\mathcal{F} = (\mathcal{V}, \mathcal{S}, \mathcal{R})$ is defined by a satisfaction relation $\vec{P}' \models (\vec{P}, \mathcal{F})$, which defines when a program \vec{P}' is a correct instrumentation of \vec{P} according to \mathcal{F} . Program

\vec{P}' must satisfy three *instrumentation conditions*: *secrecy* ($\vec{P}' \models_{\mathcal{S}} \mathcal{S}$), *transparency* ($\vec{P}' \models_{\mathcal{T}} (\vec{P}, \mathcal{R})$), and *containment* ($\vec{P}' \models_{\mathcal{C}} (\vec{P}, \mathcal{R})$), which are defined below.

Secrecy. If no execution of \vec{P}' leaks information from a source to a sink as defined by \mathcal{S} , then we say that \vec{P}' satisfies the *secrecy instrumentation condition* induced by \mathcal{S} . To state this condition formally, we first define a set of formulas that describe properties of a trace of execution T . For process p and template P , let $p \in P$ denote that p executes P in its next step of execution. Let $\text{spawned}_T(a, p)$ hold when process a spawns process p over the execution of trace T :

$$\text{spawned}_T(a, p) \equiv \exists i. T[i] = \text{SPAWN}(a, p)$$

Let $\text{IsAnc}(a, p, T)$ hold when process a is an *ancestor* of p under the spawned_T relation:

$$\text{IsAnc}(a, p, T) \equiv \text{TC}(\text{spawned}_T)(a, p)$$

where TC denotes the transitive closure operator. Let $\text{ShareAnc}(p, q, \text{Anc}, T)$ hold when processes p and q share an ancestor in Anc :

$$\text{ShareAnc}(p, q, \text{Anc}, T) \equiv \exists a \in \text{Anc}.$$

$$\text{IsAnc}(a, p, T) \wedge \text{IsAnc}(a, q, T)$$

Finally, let $\text{InfFlow}_{D, T}(p, q)$ hold when information is sent and received directly from process p to process q over the execution of trace T , where neither p or q execute a template in D :

$$\begin{aligned} \text{InfFlow}_{D, T}(p, q) \equiv & \exists i < j. ((T[i] = p!q \wedge T[j] = q?p) \\ & \vee \text{spawned}_T(p, q)) \wedge p, q \notin D \end{aligned}$$

\vec{P}' satisfies the secrecy condition induced by $\text{Secrecy}(\text{Source}, \text{Sink}, \text{Declass}, \text{Anc}) \in \mathcal{S}$ if for every execution of \vec{P}' , a process $p \in \text{Source}$ only sends information to a process $q \in \text{Sink}$ with the information flow avoiding all processes in Declass if the endpoints p and q share an ancestor process $a \in \text{Anc}$. Formally, for every trace $T \in \text{Tr}(\vec{P}')$, and every $p \in \text{Source}$ and $q \in \text{Sink}$, the following must hold:

$$\text{TC}(\text{InfFlow}_{\text{Declass}, T})(p, q) \implies \text{ShareAnc}(p, q, \text{Anc}, T)$$

If the formula holds for every secrecy assertion in \mathcal{S} , then \vec{P}' satisfies the secrecy instrumentation condition induced by \mathcal{S} , denoted by $\vec{P}' \models_{\mathcal{S}} \mathcal{S}$.

Transparency over protected flows. If an execution of \vec{P} performs only information flows that are described by the set \mathcal{R} , then this execution must be possible in \vec{P}' . We call this condition *transparency*. Formally, let $T \in \text{Tr}(\vec{P})$ be such that $\text{ProtTr}(T, \mathcal{R})$ holds, where

$$\begin{aligned} \text{ProtTr}(T, \mathcal{R}) \equiv & \forall p, q. \\ & \text{InfFlow}_{\emptyset, T}(p, q) \implies \\ & \exists \text{Prot}(\text{Source}, \text{Sink}, \text{Anc}) \in \mathcal{R}. \\ & p \in \text{Source} \wedge q \in \text{Sink} \\ & \wedge \text{ShareAnc}(p, q, \text{Anc}) \end{aligned}$$

If for every such T , it is the case that $T \in \text{Tr}(\vec{P}')$, then \vec{P}' satisfies the transparency condition induced by \mathcal{P} and \mathcal{R} , denoted by $\vec{P}' \models_{\mathcal{T}} (\vec{P}, \mathcal{R})$.

Trace containment for protected flows. Finally, an instrumented program \vec{P}' should not exhibit any behaviors solely over flows protected by \mathcal{R} that are not possible in the input program \vec{P} . We call this condition *trace containment*. Formally, let $T \in \text{Tr}(\vec{P}')$. If $\text{ProtTr}(T, \mathcal{R})$ holds, then it must be the case that $T \in \text{Tr}(\vec{P})$.

If this holds for every trace of $T \in \text{Tr}(\vec{P}')$, then \vec{P}' satisfies the containment condition induced by \vec{P} and \mathcal{R} , denoted by $\vec{P}' \models_C (\vec{P}, \mathcal{R})$.

Formal Problem Statement. The goal of the DIFC instrumenter is thus to take as input a program \vec{P} , a DIFC policy $\mathcal{F} = (\mathcal{V}, \mathcal{S}, \mathcal{R})$, and produce a program \vec{P}' such that $\vec{P}' \models_S \mathcal{S}$, $\vec{P}' \models_T (\vec{P}, \mathcal{R})$, and $\vec{P}' \models_C (\vec{P}, \mathcal{R})$. If \vec{P}' satisfies all three conditions, then it is a correct instrumentation of \vec{P} according to \mathcal{F} , denoted by $\vec{P}' \models (\vec{P}, \mathcal{F})$.

3.3 From Programs and Policies to Instrumentation Constraints

The DIFC instrumenter takes as input a program \vec{P} and policy \mathcal{F} . To produce a program \vec{P}' such that $\vec{P}' \models (\vec{P}, \mathcal{F})$, the instrumenter generates a system of set constraints such that a solution to the system corresponds to \vec{P}' . The constraints generated ensure two key properties of \vec{P}' : (1) \vec{P}' only manipulates labels in a manner allowed by the Flume reference monitor, and (2) the values of labels of all processes in all executions of \vec{P}' ensure that \mathcal{F} is satisfied.

3.3.1 Constraint Variables and Their Domain

The constraint system is defined over a set of variables, where each variable describes how a process should manipulate its label and capabilities when it executes a given template. One natural candidate for the domain of such variables is a finite set of atomic elements, where each element corresponds to a tag created by the program. However, if the DIFC instrumenter were to use such a domain, then it could not produce a program that may create an unbounded set of tags over its execution. The instrumenter thus could not handle many real-world programs and policies of interest, such as the example described in §2. The domain of the constraint variables is thus a finite set of atomic elements where each element corresponds to a tag identifier bound at a template CREATE_τ in the instrumented program.

For each CSP_{DIFC} template variable X in \vec{P} , the instrumenter generates four constraint variables: lab_X , pos_X , neg_X , creates_X . Let τ be a tag identifier. In a constraint solution, $\tau \in \text{creates}_X$, then in \vec{P}' , the template P bound to X is rewritten to $\text{CREATE}_\tau \rightarrow P$. If $\tau \in \text{lab}_X$, then the label of process $p \in X$ executing \vec{P}' contains a tag bound to τ . The analogous connection holds for variable pos_X and the positive capability of p , and the variable neg_X and the negative capability of p .

Exa. 7. *The constraint variables used by the instrumenter are illustrated in Exa. 6. Consider the templates A_5 and W . The solution in Exa. 6 defines $\text{creates}_{A_5} = \{\tau\}$. Thus the instrumenter rewrites template A_5 so that when a process executes A_5 , it creates a tag and binds the tag to identifier τ . The solution defines $\text{lab}_W = \{\tau\}$, $\text{pos}_W = \text{neg}_W = \emptyset$. Thus in the instrumented program, the label of each Worker process contains a tag bound to τ , but each Worker process cannot add or remove such a tag from its label.*

3.3.2 Generating Semantic Constraints

The instrumenter must generate a system of constraints such that any solution to the system results in DIFC code that performs actions allowed by Flume. To do so, the instrumenter constrains how a process's labels and capabilities may change over each step of its execution.

For each equation that defines the CSP_{DIFC} program, the instrumenter generates the set of constraints SemCtrs defined as follows:

$$\begin{aligned} \text{SemCtrs}(X = \text{SKIP}) &= \emptyset \\ \text{SemCtrs}(X = Y) &= \text{StepCtrs}(X, Y) \\ \text{SemCtrs}(X = \text{EVENT} \rightarrow Y) &= \text{StepCtrs}(X, Y) \\ \text{SemCtrs}(X = Y \square Z) &= \text{StepCtrs}(X, Y) \\ &\quad \cup \text{StepCtrs}(X, Z) \\ \text{SemCtrs}(X = Y \parallel Z) &= \text{StepCtrs}(X, Y) \\ &\quad \cup \text{StepCtrs}(X, Z) \end{aligned}$$

SemCtrs is defined by a function StepCtrs , which takes as input two template variables X and Y . StepCtrs generates a set of constraints that encode the relationship between the labels of a process $p \in X$ and the labels of process $p' \in Y$ that p spawns in a step of execution. One set of constraints in StepCtrs encodes that if a tag is bound to an identifier τ and is in the label of p' , then the tag must be in the label of p , or it must be in the positive capability of p' . Formally:

$$\forall \tau. \tau \in \text{lab}_Y \implies \tau \in \text{lab}_X \vee \tau \in \text{pos}_Y$$

Equivalently:

$$\text{lab}_Y \subseteq \text{lab}_X \cup \text{pos}_Y$$

Additionally, if a tag is bound to τ in the label of p and is not in the negative capability of p' , then the tag must be in the label of p' . Formally:

$$\forall \tau. \tau \in \text{lab}_X \wedge \tau \notin \text{neg}_Y \implies \tau \in \text{lab}_Y$$

Equivalently:

$$\text{lab}_Y \supseteq \text{lab}_X - \text{neg}_Y$$

The other constraints in StepCtrs encode that the capabilities of p' may only grow by the capabilities of tags that p' creates. If p' has a positive (negative) capability for a tag bound to an identifier τ , then either p must have the positive (negative) capability for the tag, or the tag must be created and bound to τ at p' . Formally:

$$\begin{aligned} \forall \tau. \tau \in \text{pos}_Y &\implies \tau \in \text{pos}_X \vee \tau \in \text{creates}_Y \\ \wedge \tau \in \text{neg}_Y &\implies \tau \in \text{neg}_X \vee \tau \in \text{creates}_Y \end{aligned}$$

Equivalently:

$$\begin{aligned} \text{pos}_Y &\subseteq \text{pos}_X \cup \text{creates}_Y \\ \text{neg}_Y &\subseteq \text{neg}_X \cup \text{creates}_Y \end{aligned}$$

Finally, the instrumenter constrains that no tag identifier τ is bound at multiple templates. Formally:

$$\forall X, Y, \tau. X \neq Y \implies \tau \notin \text{creates}_X \cap \text{creates}_Y$$

Equivalently:

$$\bigwedge_{X \neq Y \in \text{Vars}(\vec{P})} \text{creates}_X \cap \text{creates}_Y = \emptyset$$

The instrumenter conjoins these constraints with the constraints generated from applying SemCtrs to all equations in \vec{P} to form a system of constraints φ_{Sem} . Any solution to φ_{Sem} corresponds to a program in which each process manipulates labels as allowed by Flume.

3.3.3 Generating Policy Constraints

The instrumenter must constrain that the instrumented program satisfies the instrumentation conditions of §3.2.2. To do so, the instrumenter generates constraints for each flow assertion in the policy.

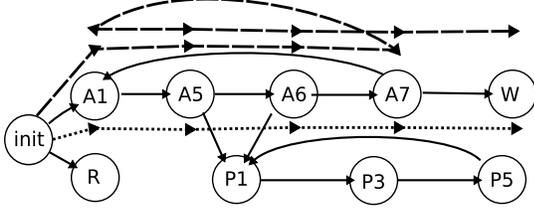


Figure 4. The spawn graph of the server from Fig. 1. The dotted and dashed paths denote process executions that invalidate `init` as a template to create tags that isolate Workers.

First, suppose that the instrumenter is given a secrecy assertion $\text{Secrecy}(\text{Source}, \text{Sink}, \text{Declass}, \text{Anc})$. The assertion induces a secrecy instrumentation condition. To instrument the program to respect this condition, the instrumenter must assert that, for processes $p \in \text{Source}$ and $q \in \text{Sink}$ that do not share an ancestor in `Anc`, information should not flow from p to q solely through processes that are not in `Declass`. We describe how the instrumenter asserts this by considering example executions that would violate the secrecy assertion. To describe these executions, we use the *spawn graph* of a program:

Def. 1. For a CSP_{DIFC} program \vec{P} , the **spawn graph** of \vec{P} is a graph that represents the “spawns” relation over process templates. Formally, the spawn graph of \vec{P} is $G_{\vec{P}} = (N, E)$, where for every template variable P , node $n_P \in N(G_{\vec{P}})$, and $(n_P, n_Q) \in E(G_{\vec{P}})$ if and only if a process $p \in P$ may spawn process $p' \in Q$.

The spawn graph of the program from Fig. 1 is given in Fig. 4.

Exa. 8. Consider the server from Fig. 1 and the secrecy assertion that no Workers executing `W` should be able to communicate information to each other unless the information flows through a proxy: $\text{Secrecy}(W, W, \{P_1, P_2, P_3\}, W)$. Suppose that the instrumenter generated no constraints to ensure that the instrumented program followed this assertion. The instrumenter might then instrument the program to create no tags. One execution of the program could then create a Worker process p by executing the series of templates `init`, `A1`, `A5`, `A6`, `A7`, and `W` (the dotted path in Fig. 4), and create another Worker process q by executing templates `init`, `A1`, `A5`, `A6`, `A7`, `A1`, `A5`, `A6`, `A7`, and `W` (the dashed path in Fig. 4). The label of p would then be a subset of the label of q , and thus p could send information to q .

To guard against executions such as those in Exa. 8 for a general assertion $\text{Secrecy}(\text{Source}, \text{Sink}, \text{Declass}, \text{Anc})$, the instrumenter could generate the constraint $\text{lab}_{\text{Source}} \not\subseteq \text{lab}_{\text{Sink}}$. However, this constraint may not allow the instrumenter to find valid instrumentations of the program in important cases.

Exa. 9. Suppose that for a general secrecy assertion $\text{Secrecy}(\text{Source}, \text{Sink}, \text{Declass}, \text{Anc})$, the instrumenter generated the constraint $\text{lab}_{\text{Source}} \not\subseteq \text{lab}_{\text{Sink}}$. Then for the server of Fig. 1 and secrecy assertion of Exa. 8, the resulting constraint, $\text{lab}_W \not\subseteq \text{lab}_W$, is unsatisfiable, and the instrumenter would fail to instrument the server. However, if the instrumenter rewrote the server to create a tag each time a process executed template `A1`, bind the tag to an identifier τ , and place the tag in the label of the next Worker spawned, then all Worker processes would be isolated.

By contrast, if the instrumenter rewrote the server to create a tag each time a process executed template `init`, bind the tag to an identifier τ , and place the tag in the label of the next Worker spawned, then there would be executions of the instrumented program in which Worker processes were not isolated. For example,

the Workers described in Exa. 8 would have in their labels the same tag bound to τ , and thus would be able to communicate.

Thus there is a key distinction between templates `A1` and `init`: if p and q are distinct Worker processes, then they cannot share the same tag created at `A1`. However, they can share the same tag created at `init`.

The instrumenter captures the distinction between `A1` and `init` in Exa. 9 for a general secrecy assertion $\text{Secrecy}(\text{Source}, \text{Sink}, \text{Declass}, \text{Anc})$ by constraining that there is a tag identifier $\tau \in \text{lab}_{\text{Source}}$ such that $\tau \notin \text{lab}_{\text{Sink}}$ or τ must be bound at a template in Dist_{Anc} , where Dist_{Anc} is defined as follows:

Def. 2. Let P and Q be process templates. Q is **distinct** for P , denoted by $Q \in \text{Dist}_P$, if and only if the following holds. Let Q bind tags that it creates to a tag identifier τ , and let r, s be distinct processes with distinct ancestors in P . If τ is bound to a tag t_1 in the namespace of r and τ is bound to a tag t_2 in the namespace of s , then $t_1 \neq t_2$.

To instrument a program to satisfy a secrecy assertion, the instrumenter could thus weaken the constraint $\text{lab}_{\text{Source}} \subseteq \text{lab}_{\text{Sink}}$ from above to $\text{lab}_{\text{Source}} \not\subseteq \text{lab}_{\text{Sink}} - \bigcup_{Q \in \text{Dist}_{\text{Anc}}} \text{creates}_Q$. However, a program instrumented using such a constraint may still allow flows from a process $p \in \text{Source}$ to a process $q \in \text{Sink}$ not allowed by the assertion. The program could do so by allowing processes not in `Declass` to receive information from p , remove tags associated with the information, and then send the information to q . To guard against this, the instrumenter strengthens the above constraint to:

$$\text{lab}_{\text{Source}} \not\subseteq \left(\text{lab}_{\text{Sink}} - \bigcup_{Q \in \text{Dist}_{\text{Anc}}} \text{creates}_Q \right) \cup \bigcup_{D \notin \text{Declass}} \text{neg}_D$$

We prove in [8, App. F] that this constraint is sufficient to ensure that the instrumented program satisfies the secrecy assertion.

Now suppose that the instrumenter is given a protection assertion $\text{Prot}(\text{Source}, \text{Sink}, \text{Anc})$. The assertion induces transparency and instrumentation conditions. To instrument the program to respect these conditions, the instrumenter must assert that whenever a process $p \in \text{Source}$ communicates data to a process $q \in \text{Sink}$ where p and q share an ancestor process $a \in \text{Anc}$, then the communication must be successful. To assert this, the instrumenter must ensure that every tag t in the label of p is also in the label of q . We describe how the instrumenter does so by considering example executions that violate protection assertions.

Exa. 10. Consider the server from Fig. 1 and the protection assertion $\text{Prot}(P_5, R, \text{init})$ that each Proxy executing `P5` must be able to send information to the Requester executing `R`. Suppose that the instrumenter generated no constraints to ensure that the program followed this assertion. The instrumenter might then instrument the program to bind a tag to an identifier τ at template `A1`. One execution of the program could create a Proxy process p by executing the series of templates `init`, `A1`, `A5`, `A6`, `P1`, `P3`, and `P5`, and create a Requester process q by executing the series of templates `init` and `R`. Suppose that p had in its label the tag that was bound to τ when its ancestor executed `A1`. Because no ancestor of q executed `A1`, q would not have a tag in its label bound to τ . Thus the label of p would not be a subset of the label of q , and p would fail to communicate to q .

Exa. 10 demonstrates that for assertion $\text{Prot}(\text{Source}, \text{Sink}, \text{Anc})$, if a tag in the label of $p \in \text{Source}$ is bound to an identifier τ , then for p to send information to $q \in \text{Sink}$, there must be a tag in the label of q that is bound to τ . This is expressed as $\text{lab}_{\text{Source}} \subseteq \text{lab}_{\text{Sink}}$. However, this constraint is not sufficient to ensure that p and q communicate, as demonstrated by the following example.

Exa. 11. Suppose that the server in Fig. 1 was instrumented to bind a tag to an identifier τ at A_1 , add this tag to the next Proxy process, and add the tag to the label of the Requester process executing R . Each Proxy process executing P_3 would have a different ancestor that executed A_1 , and thus each Proxy would have a different tag in its label. Although $\text{lab}_{P_3} = \{\tau\} \subseteq \{\tau\} = \text{lab}_R$, because each tag bound to τ in the label of each Proxy process executing P_3 is distinct, the label of the Requester process does not contain the label of all processes executing P_3 . Thus communication from Proxy processes to the Requester could fail.

On the other hand, suppose that the server was instrumented to bind a tag to τ at init , and add this tag to the label of Proxy processes executing P_3 and the Requester executing R . Then the same tag would be in the labels of each Proxy process and the Requester. The key distinction between A_1 and init is that a Proxy and Requester may have distinct tags created at A_1 , but cannot have distinct tags created at init .

The instrumenter captures the distinction between init and A_1 in Exa. 11 for a general assertion $\text{Prot}(\text{Source}, \text{Sink}, \text{Anc})$ by strengthening the above constraint that $\text{lab}_{\text{Source}} \subseteq \text{lab}_{\text{Sink}}$ to constraining that if a tag in the label of $p \in \text{Source}$ is bound to an identifier τ , then $\tau \in \text{lab}_{\text{Sink}}$ and τ must be bound at a template in $\text{Const}_{\text{Anc}}$, where $\text{Const}_{\text{Anc}}$ is defined as follows.

Def. 3. Let P, Q be process templates. Q is **constant** for P , denoted $Q \in \text{Const}_P$, if and only if the following holds. Let processes r and s share in common their most recent ancestor in P , and let Q bind tags to a tag identifier τ . If τ is bound to a tag t_1 in the namespace of r , and τ is bound to a tag t_2 in the namespace of s , then $t_1 = t_2$.

For a protection assertion $\text{Prot}(\text{Source}, \text{Sink}, \text{Declass})$, the conditions on each tag identifier τ are expressed formally using $\text{Const}_{\text{Anc}}$ as:

$$\forall \tau. \tau \in \text{lab}_{\text{Source}} \implies \tau \in \text{lab}_{\text{Sink}} \wedge \tau \in \bigcup_{Q \in \text{Const}_{\text{Anc}}} \text{creates}_Q$$

Equivalently:

$$\text{lab}_{\text{Source}} \subseteq \text{lab}_{\text{Sink}} \cap \bigcup_{Q \in \text{Const}_{\text{Anc}}} \text{creates}_Q$$

We prove in [8, App. F] that this constraint is sufficient to ensure that the instrumented program satisfies the protection assertion.

The definitions of Dist and Const given in Defn. 2 and Defn. 3 explain how the sets are used to instrument a program, but they do not describe how the sets may be computed. The sets are computed through a series of reachability queries over the spawn graph of the program. For further details, see App. A.

A solution to the conjunction φ_{Pol} of constraints generated for all flow assertions in a policy \mathcal{F} corresponds to an instrumentation that respects all assertions. A solution to the conjunction of these constraints with the semantic constraints, $\varphi_{\text{Tot}} \equiv \varphi_{\text{Sem}} \wedge \varphi_{\text{Pol}}$, thus corresponds to a program that manipulates Flume labels legally to satisfy \mathcal{F} .

3.4 Solving Instrumentation Constraints

After generating a system of constraints φ_{Tot} as described in §3.3, the instrumenter must find a solution to φ_{Tot} , and from the solution instrument \vec{P} . Unfortunately, such systems are computationally difficult to solve in general; finding a solution to φ_{Tot} is NP-complete in the number of terms in φ_{Tot} . We give a proof of hardness in [8, App. G].

However, although such systems are hard to solve in general, they can be solved efficiently in practice. Modern Satisfiability Modulo Theory (SMT) solvers [7] can typically solve large logical

formulas very quickly. To apply an SMT solver, the instrumenter must translate φ_{Tot} from a formula over a theory of set constraints to a formula over a theory supported by the solver, such as the theory of bit-vectors. To translate φ_{Tot} , the instrumenter must derive for φ_{Tot} a bound B such that if φ_{Tot} has a solution, then it has a solution in which the value of each constraint variable contains at most B elements. Such a bound B always exists, and is equal to the number of secrecy flow assertions. We prove the validity of this bound and give the explicit rules for translating set constraints to bit-vector constraints in App. B.

The instrumenter applies an SMT solver to the bit-vector translation of the set-constraint system. If the SMT solver determines that the formula is unsatisfiable, then it produces an unsatisfiable core of bit-vector constraints. The core is a subset of the original constraint system that is unsatisfiable, and does not strictly contain an unsatisfiable subset. Given such a core, the instrumenter computes the subprogram and flow assertions that contributed constraints in the core, and presents these to the user. If the SMT solver determines that the constraint system is satisfiable, then the instrumenter rewrites the program so that the label values of all processes that execute the instrumented program correspond to the label values in the constraint solution.

3.5 From Constraint Solutions to Instrumented Programs

For a program \vec{P} and policy \mathcal{F} , if the instrumenter obtains a solution to the constraint system φ_{Tot} described in §3.3.3, then from this solution it rewrites \vec{P} to a new program \vec{P}' that respects \mathcal{F} . Each equation $X = P$ in \vec{P} is rewritten as follows. If creates_X contains a tag identifier τ , then the instrumenter rewrites P to $\text{CREATE}_\tau \rightarrow P$. Now, suppose that L, M , and N are the sets of tag identifiers in the constraint values for lab_X , pos_X , and neg_X . Then the instrumenter rewrites P to $\text{ChangeLabel}(L, M, N) \rightarrow P$. The instrumenter can reduce the number of ChangeLabel templates generated by only generating such a template when a label or positive capability changes from that of a preceding P template in $G_{\vec{P}}$.

The instrumenter is sound in the sense that if it produces an instrumented program, then the program satisfies the instrumentation conditions of §3.2. However, it is not complete; e.g., to satisfy some programs and policies, it could be necessary for different processes executing the same template to contain tags created at different templates. This behavior is not supported by the instrumenter. However, our experiments, described in §4, indicate that in practice, the instrumenter can successfully instrument real-world programs to handle real-world policies.

4. Experiments

We evaluated the effectiveness of the DIFC instrumenter by experimenting with four programs. The experiments were designed to determine whether, for real-world programs and policies,

- the instrumenter is expressive: can its language of policies encode real-world information-flow policies, and can the instrumenter rewrite real programs to satisfy such policies? We found that each of the real-world policies could be encoded in the language of the instrumenter, and that the instrumenter could find a correct instrumentation of the program with minimal, if any, manual edits of the program.
- the instrumenter is efficient and scalable: can it instrument programs quickly enough to be used as a practical tool for developing applications? We found that the instrumenter could instrument programs in seconds.

To examine these properties, we implemented the DIFC instrumenter as an automatic tool² called SWIM³ and applied it to instrument the following program modules:

1. The multi-process module of Apache [1].
2. The CGI and untrusted code launching modules of FlumeWiki [12].
3. The scanner module of ClamAV [4].
4. The OpenVPN client [16].

For each program module, we chose an information-flow policy from the literature [12, 21], expressed the policy in terms of the flow assertions described in §3.2.2, and then fed the program and policy to the tool.

We implemented SWIM using the CIL [15] program-analysis infrastructure for C, and the Yices SMT solver [7]. The only program annotations required by SWIM are C labels (*not* Flume labels) that map program points to variables used in flow assertions. When successful, SWIM outputs a C program instrumented with calls to the Flume API such that the program satisfies the input policy. We performed all experiments using SWIM on a machine with a 2.27 GHz 8-core processor and 6 GB of memory, although SWIM uses only a single core.

We first describe our experience using SWIM, and then evaluate its performance.

Apache Multi-Process Module. We applied SWIM to the multi-process module of the Apache web-server to automatically produce a version of Apache that isolates *Worker* processes. A model of the Apache system architecture, along with the desired policy, serves as the example described in §2. When we initially applied SWIM to Apache and its policy, SWIM was unable to find an instrumentation. Indeed, it was impossible to instrument Apache to isolate *Worker* processes, because its MPM process establishes a direct connection between the process that issues a request and the *Worker* process spawned to service a request. We thus modified the MPM code by hand to spawn *uninstrumented* proxy processes, which forward information between *Worker* and requester, as described in §1 and §2. When we applied the instrumenter to the modified program, it instrumented the new program so that it satisfied the policy. Although it is unfortunate that we had to modify the program manually, we were able to use the instrumenter to determine that such a modification was necessary. Moreover, the modification was relatively simple, and in the future, it may be possible to automatically rewrite a program as necessary with simple objects such as connection proxies to find a correct instrumentation.

FlumeWiki CGI and Untrusted Code Modules. We applied SWIM to FlumeWiki modules that launch processes to service requests, producing a version of FlumeWiki in which each process that services a request acts with exactly the DIFC permissions of the user who makes the request. FlumeWiki [12] is based on the software package MoinMoin Wiki [13], but has been extended to run on the Flume operating system with enhanced security guarantees. Similar to Apache, in FlumeWiki a *launcher* process receives requests from users for generating CGI forms, running potentially untrusted code, or interacting with the Wiki. The launcher then spawns an untrusted *Worker* to service the request. However, whereas Apache should execute with no information flowing from one *Worker* to another, in FlumeWiki each *Worker* should be able to access exactly the files that can be accessed by the user who issued the request. To express this policy and instrument FlumeWiki to satisfy it, we used policies defined over *persistent principals* (e.g. users). The semantics of these policies and the instrumenter’s technique for generating code that satisfies them is analogous to how it generates code to handle the policies of §3.2. We give fur-

ther details in [8, App. H]. We removed the existing DIFC code from the modules of FlumeWiki that launch processes that serve CGI forms or run untrusted code. We then applied SWIM to the uninstrumented program and policy. SWIM instrumented the program correctly, with code that was similar to the original, manually written code.

ClamAV Virus Scanner Module. We applied SWIM to ClamAV to automatically produce a virus scanner that is guaranteed not to leak sensitive data over a network or other output device, even if it is compromised. ClamAV is a virus-detection tool that periodically scans the files of a user, checking for the presence of viruses by comparing the files against a database of virus signatures. To function correctly, ClamAV must be trusted to read the sensitive files of a user, yet a user may want assurance that even if a process running ClamAV is compromised, it will not be able to send sensitive data over a network connection.

Inspired by [21], we modeled a system running ClamAV using the “scanner” module of ClamAV, a file containing sensitive user data, a file acting as a user TTY, a proxy between the scanner and the TTY, a file acting as a virus database, a process acting as a network connection, a process acting as a daemon that updates the virus database, and a process that spawns the scanner and update daemon and may set the labels of all processes and files. We then wrote a policy of nine flow assertions that specified that:

- The update daemon should always be able to read and write to the network and virus database.
- The scanner should always be able to read the sensitive user data and virus database.
- The scanner should never be able to send data directly to the network or TTY device. However, it should always be able to send data to the proxy, which should always be able to communicate with the TTY device.

SWIM automatically instrumented the model so that it satisfies the policy. Although we only used SWIM to instrument one, arbitrarily chosen system configuration, because SWIM is able to instrument systems very quickly, it could easily be used to reinstrument a system as the configuration of the system changes.

OpenVPN. We applied SWIM to OpenVPN to automatically produce a system that respects *VPN isolation*. OpenVPN is an open-source VPN client. Because VPNs act as a bridge between networks on both sides of a firewall, they represent a serious security risk [21]. A common desired policy for systems running a VPN client is VPN isolation, which specifies that information should be not able to flow from one side of a firewall to the other unless it passes through the VPN client.

We modeled a system running OpenVPN using the code of the entire OpenVPN program, files modeling networks on opposing sides of a firewall ($Network_1$ and $Network_2$), and a process (*init*) that launches OpenVPN and may alter the labels of the networks. We expressed VPN isolation for this model as a set of six flow assertions that specified that:

- Information show not flow between $Network_1$ and $Network_2$ unless it flows through OpenVPN.
- OpenVPN should always be able to read to and write from $Network_1$ and $Network_2$.

SWIM automatically instrumented the model so that it satisfies the policy. As in the case with ClamAV, we applied SWIM to one particular configuration of a system running OpenVPN, but SWIM is fast enough that it can easily be reapplied to a system running OpenVPN as the system’s configuration changes.

Our experience using SWIM indicates that the DIFC instrumenter is sufficiently expressive to instrument real-world programs to satisfy real-world policies. While the flow assertions presented in §3.2 are simple to state, they can be combined to express complex,

² Available at <http://cs.wisc.edu/~wrharris/software/difc>

³ Secure What I Mean

Program Name	LoC	Time (s)	Num. Inst.
Apache (MPM)	15,409	2.302	49
FlumeWiki (CGI)	300	0.183	46
FlumeWiki (WC)	286	0.096	34
ClamAV (scanner)	10,919	1.374	117
OpenVPN	98,262	7.912	51

Table 2. Performance of the DIFC instrumenter.

realistic policies. While not all programs could be instrumented to satisfy a desired policy without modification, when an instrumentation does exist, the instrumenter was able to find it each time.

For each application, we empirically measured the performance of SWIM. Results are given in Tab. 4. Col. “LoC” gives the number of lines of code in the program modules given to SWIM. Col. “Time (s)” gives the time in seconds required for the instrumenter to instrument the program. Col. “Num. Inst.” gives the number of statements instrumented by SWIM. The results indicate the DIFC instrumenter is a practical technique: SWIM is able to instrument large, real-world program modules in seconds. Thus it is fast enough even to be integrated into the edit-compile-run cycle of the software-development work cycle.

5. Related Work

Multiple operating systems support DIFC, including Asbestos [19], HiStar [21], and Flume [12]. These systems all provide low-level mechanisms that allow an application programmer to implement an information-flow policy. Our instrumenter complements these systems. Although we have restricted the discussion to Flume, our instrumenter can be easily generalized to allow for program transformations to instrument programs for other DIFC operating systems. By running automatically-instrumented programs on top of a DIFC operating system, a user obtains greater assurance of the end-to-end information-flow security of their application. Note that the Asbestos system attempts to lessen the amount of DIFC code in applications by implicitly redefining label values to allow communication when the process is capable of performing the redefinition. This has multiple disadvantages. Krohn and Tromer [11] demonstrate that such systems may allow forbidden information flows. Furthermore, such implicit behaviors can make programs more difficult to reason about.

Harris et. al. [9] apply a model checker for safety properties of concurrent programs to determine if a fully instrumented DIFC application satisfies a high-level information flow policy. The present paper describes how to instrument DIFC code automatically, given only an uninstrumented program and a policy. Such code is correct by construction. Krohn and Tromer [11] use CSP to reason about the Flume OS, not applications running atop Flume.

Resin [20] is a language runtime that allows a programmer to specify dataflow assertions, which are checked over the state of the associated data before the data is allowed to be sent from one system object to another. Resin allows for arbitrary code to be run on certain events, but it does not attempt to provide guarantees that an application satisfies a high-level policy. In comparison, our policy language is less expressive, but the code generated by our approach is correct by construction. Additionally, DIFC systems provide certain guarantees that Resin does not match [20].

Previous work describes techniques to automatically synthesize programs from complete specifications of their behavior [6, 17]. Like our instrumenter, these techniques assume a program skeleton and a specification of correctness, and then use constraint solving to generate language constructs, yielding a concrete implementation of the specification. However, these techniques synthesize single-

process arithmetic programs, while our instrumenter rewrites programs that may execute over an unbounded set of processes.

Several programming languages, such as Jif, provide type systems based on security labels that allow the programmer to validate security properties of their code through type-checking [14, 18]. Jif has been used to implement several real-world applications with strong security guarantees (e.g. [5, 3, 10]), but these programs are written from scratch in Jif. Automatic techniques can partition a Jif web application between its client and server [3], but the implementation of the composite program was done manually prior to partitioning. Our instrumenter adds labeling code to complete applications developed without label code.

6. Conclusion

Until now, the promise of DIFC operating systems has been limited by the added burden that they place on application programmers. We have presented a technique that takes a DIFC-unaware application and an information-flow policy and automatically instruments the application to satisfy the policy, while respecting the functionality of the application. Our technique thus greatly improves the applicability of DIFC systems and the end-to-end reliability of applications that run on such systems.

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A. Computing Dist_P and Const_P

The definition of Dist given in Defn. 2 explains how Dist sets may be used to formulate constraints for Secrecy assertions, but it does not describe how Dist sets may be computed. For a program \vec{P} with template P , the set Dist_P is computed by applying the following theorem and issuing a series of reachability queries over \vec{P} 's spawn graph $G_{\vec{P}}$:

Thm. 1. For program \vec{P} that contains process templates P and Q , $Q \in \text{Dist}_P$ if and only if \vec{P} contains no template variable R such that:

1. n_Q reaches n_R .
2. $\vec{P}(R) = S \parallel T$, where n_S and n_T reach n_P without going through n_Q , or n_P reaches n_R and n_R reaches n_P without going through n_Q .

Proof. We first prove that if no such R exists, then $Q \in \text{Dist}_P$. Let $T \in \text{Tr}(\vec{P})$ be a trace of an arbitrary execution. If T has a subsequence $\text{SPAWN}(p_0, p_1), \text{SPAWN}(p_1, p_2), \dots, \text{SPAWN}(p_{n-1}, p_n)$, then we say that T has a spawn trace $p_0 \rightarrow p_1 \rightarrow \dots \rightarrow p_n$. All processes that execute in T start from a root process template A . Consider processes d_1 and d_2 with spawn traces $a \rightarrow \dots \rightarrow q \rightarrow \dots \rightarrow p_1 \rightarrow \dots \rightarrow \dots d_1$ and $a \rightarrow \dots \rightarrow q \rightarrow \dots \rightarrow p_2 \rightarrow \dots \rightarrow d_2$ with $a \in A, q \in Q$, and $p_1, p_2 \in P$ where d_1 and d_2 share the tag created at q in their namespaces. We want to show that $p_1 = p_2$.

Suppose on the contrary that $p_1 \neq p_2$. Because the spawn traces of d_1 and d_2 start from the same process a , there must be some processes r, s, t such that $r \rightarrow s$ is in the trace of d_1 while $r \rightarrow t$ is in the spawn trace of d_2 , with $s \neq t$. Let r, s, t be the first processes for which this is the case. First, suppose that r spawns before both p_1 and p_2 . Then the spawn traces of d_1 and d_2 have subtraces $q \rightarrow \dots \rightarrow r \rightarrow s \rightarrow \dots \rightarrow p_1 \rightarrow \dots \rightarrow d_1$ and $q \rightarrow \dots \rightarrow r \rightarrow t \rightarrow \dots \rightarrow p_2 \rightarrow \dots \rightarrow d_2$. By hypothesis, d_1 and d_2 share the same tag created at Q , so neither subtrace executes a process in Q other than q . However, this implies that r executes a template R such that $\vec{P}(R) = S \parallel T$, and n_S and n_T reach n_P without going through n_Q . Thus R satisfies the conditions of Thm. 1, which violates our hypothesis. Thus r cannot spawn before p_1 and p_2 .

Now suppose that r spawns after p_1 , but before p_2 . Then the spawn traces of d_1 and d_2 have subtraces $q \rightarrow \dots \rightarrow p_1 \rightarrow \dots \rightarrow r \rightarrow \dots \rightarrow d_1$ and $q \rightarrow \dots \rightarrow r \rightarrow \dots \rightarrow p_2 \rightarrow \dots \rightarrow d_2$ with q the only process in Q , $p_1, p_2 \in P$, and $r \in R$. These subtraces imply that n_P reaches n_R and n_R reaches n_P without going through n_Q . Thus R satisfies the conditions of Thm. 1, and violates our hypothesis. r may not spawn before p_1 and after p_2 , by a symmetric argument. Finally, if r spawned after p_1 and p_2 , then it would be the case that $p_1 = p_2$, which violates our hypothesis. Thus no such r can exist, and it must be the case that $p_1 = p_2$.

We now prove that if R does exist, then $Q \notin \text{Dist}_P$. First, suppose that $\vec{P}(R) = S \parallel T$, where n_S and n_T both reach n_P . Then \vec{P} has an execution with processes d_1 and d_2 with spawn subtraces $q \rightarrow \dots \rightarrow r \rightarrow s \rightarrow \dots \rightarrow p_1 \rightarrow \dots \rightarrow d_1$

and $q \rightarrow \dots \rightarrow r \rightarrow t \rightarrow \dots \rightarrow p_2 \rightarrow d_2$ that contain no processes in Q after q , with $r \in R, s \in S, t \in T$, and $p_1, p_2 \in P$. Thus d_1 and d_2 have distinct ancestor processes in P , but share a tag created at Q , and $Q \notin \text{Dist}_P$ by definition. Now suppose that n_P reaches n_R , and n_S reaches n_P without going through n_Q . Then \vec{P} has an execution with processes d_1 and d_2 with spawn subtraces $q \rightarrow \dots \rightarrow p_1 \rightarrow \dots \rightarrow r \rightarrow t \rightarrow d_1$ and $q \rightarrow \dots \rightarrow p_1 \rightarrow r \rightarrow s \rightarrow \dots \rightarrow p_2 \rightarrow d_2$ where $p_1 \neq p_2 \in P, r \in R, s \in S, t \in T$, and $s \neq t$. Neither subtrace contains a process in Q aside from the common q at the start of both traces, so d_1 and d_2 have distinct ancestor processes $p_1, p_2 \in P$, but share a tag created at Q . In the case that n_T instead of n_S reaches n_P , two processes d_1 and d_2 have distinct ancestor processes $p_1, p_2 \in P$ but share a tag created at Q , by a symmetric arguments. Thus by definition, $Q \notin \text{Dist}_P$. \square

The definition of Const given in Defn. 3 explains how Const sets may be used to formulate constraints for Prot assertions, but it does not describe how Const sets may be computed. For a program \vec{P} with template P , the set Const_P is computed by applying the following theorem and issuing reachability queries over \vec{P} 's spawn graph $G_{\vec{P}}$:

Thm. 2. For program \vec{P} that contains process templates P and Q , $Q \in \text{Const}_P$ if and only if \vec{P} contains no template R such that:

1. n_P reaches n_R in $G_{\vec{P}}$.
2. $\vec{P}(R) = S \parallel T$, where n_S or n_T reach n_Q without going through n_P .

Proof. We first prove that if there is no such R , then $Q \in \text{Const}_P$. Let d_1, d_2 be distinct processes with spawn traces $a \rightarrow \dots \rightarrow p \rightarrow \dots \rightarrow r \rightarrow s \rightarrow \dots \rightarrow d_1$ and $a \rightarrow \dots \rightarrow p \rightarrow \dots \rightarrow r \rightarrow t \rightarrow \dots \rightarrow d_2$, where $a \in A, q \in Q, r \in R, s \in S, t \in T$, and $s \neq t$. By hypothesis, neither of the subtraces $s \rightarrow \dots \rightarrow p_1$ nor $t \rightarrow \dots \rightarrow p_2$ may execute Q , or R would satisfy the conditions of Thm. 2. Thus if d_1 has a tag created at Q , then it is the same tag that r has, which is the same tag that p_2 has.

We now prove that if such an R does exist, then $Q \notin \text{Const}_P$. Suppose that n_S reaches n_Q , and the spawn traces from above have subtraces $s \rightarrow \dots \rightarrow q \rightarrow \dots \rightarrow d_1$ and $t \rightarrow \dots \rightarrow d_2$, where $q \in Q$. When q executes Q , it creates a fresh tag u and binds u in its namespace. The only way that u may become unbound in the namespace is if a subsequent process executes Q again, binding a new fresh tag in the namespace. The most recent such tag is bound in the namespace of d_1 . Thus d_1 cannot share a tag created by d_2 , and $Q \notin \text{Const}_P$. By a symmetric argument, if n_T reaches n_Q , then $Q \notin \text{Const}_P$. \square

B. Translating Set Constraints to Bit-vector Constraints

B.1 Bounding the Number of Abstract Tags in the Domain

To translate a system of set constraints φ to a system of bit-vector constraints, the instrumenter must derive a bound B such that if φ has a solution, then it has one in which the value of each variable has at most B elements. We will prove that B is equal to the number of negative set constraints in φ . To do so, we will prove a theorem over a general class of set constraints that include those that may be generated by the instrumenter.

Thm. 3. Let φ be a conjunction of constraints, where each constraint is of the form $X \subseteq f(\vec{X})$ or $Y \not\subseteq g(\vec{Y})$, where X and Y are single variables, and $f(\vec{X})$ and $g(\vec{Y})$ are set expressions built

only from the sets of variables \vec{X} and \vec{Y} using set union, intersection, and difference. If φ has a solution, then it has one in which the value for each variable contains at most one element for each negative subset constraint.

Proof. Let s be a solution to φ , which maps each variable in φ to a set of atomic elements, and extends to set expressions in the natural way. For each negative subset constraint $Y \not\subseteq g(\vec{Y})$, pick one element $t \in s(Y) - s(g(\vec{Y}))$. Let E be the set of all such chosen elements, and for each variable X , let $s'(X) = s(X) \cap E$. s' is a solution to φ . To see this, first note that $s'(f(\vec{X})) = s(f(\vec{X})) \cap E$. This follows by the distributivity of intersection over union, intersection, and difference. To see that s' is a solution for any positive constraint in φ , observe that

$$s'(X) = s(X) \cap E \subseteq s(f(\vec{X})) \cap E = s'(f(\vec{X}))$$

To see that s' is a solution for any negative constraint in φ , observe that

$$s'(Y) = s(Y) \cap E \not\subseteq s(g(\vec{Y})) \cap E = s'(g(\vec{Y}))$$

The non-containment follows from the fact that by the definition of E , $s(Y)$ contains an element in E that is not in $s(g(\vec{Y}))$. \square

B.2 From Set Constraints to Bit-vector Constraints

Using the bound B described in App. B.1, the instrumenter translates set-constraint system φ to a bit-vector constraint system φ_{bv} such that if φ_{bv} has a solution, then the solution can be translated efficiently into a solution for φ , and if φ_{bv} does not have a solution, then φ does not have a solution. To perform this translation, the instrumenter applies the following rules to each constraint in φ :

$$\begin{aligned} \llbracket \text{term}_1 \subseteq \text{term}_2 \rrbracket_C^B &\equiv \llbracket \text{term}_1 \rrbracket_T^B \& \llbracket \text{term}_2 \rrbracket_T^B = \llbracket \text{term}_1 \rrbracket_T^B \\ \llbracket \text{term}_1 \not\subseteq \text{term}_2 \rrbracket_C^B &\equiv \neg \llbracket \text{term}_1 \subseteq \text{term}_2 \rrbracket_C^B \\ \llbracket X \rrbracket_T^B &\equiv X_{bv}^B \\ \llbracket \text{term}_1 \cup \text{term}_2 \rrbracket_T^B &\equiv \llbracket \text{term}_1 \rrbracket_T^B \mid \llbracket \text{term}_2 \rrbracket_T^B \\ \llbracket \text{term}_1 \cap \text{term}_2 \rrbracket_T^B &\equiv \llbracket \text{term}_1 \rrbracket_T^B \& \llbracket \text{term}_2 \rrbracket_T^B \\ \llbracket \text{term}_1 - \text{term}_2 \rrbracket_T^B &\equiv \llbracket \text{term}_1 \rrbracket_T^B \& \llbracket \overline{\text{term}_2} \rrbracket_T^B \end{aligned}$$

In the above rules, term is an arbitrary term in the theory of set constraints. $\llbracket \cdot \rrbracket_C^B$ translates a set constraint to a constraint over bit-vectors of size B . It is defined using $\llbracket \cdot \rrbracket_T^B$, which translates a set term to be a term defined over bit-vectors of size B . Variable X_{bv}^B denotes a bit-vector variable of size B that corresponds one-to-one with the set variable X , $\&$ denotes bit-wise AND, \mid denotes bit-wise OR, and \overline{X} denotes the bit-wise complement of X .

C. Modeling Compromised Processes

A key strength of DIFC operating systems is that they allow a programmer to restrict the flow of their program's information, even if the information flows through a process that may be compromised. Obviously, the DIFC instrumenter cannot hope to instrument program segments that may become compromised and expect for the code to be run in the event of a compromise. However, the instrumenter can in many cases produce programs that soundly handle compromised processes.

To instrument code that may have compromised processes, we revise the constraints generated for policies. Prot assertions are given to ensure that the DIFC system preserves some aspect of the original program's functionality. It is hopeless to try to preserve functionality once a process has been compromised, so even if we know that processes mentioned in a Prot assertion may be compromised, the instrumenter does not rewrite the generated constraints.

However, suppose the instrumenter is given a secrecy assertion $\text{Secrecy}(\text{Source}, \text{Sink}, \text{Declass}, \text{Anc})$, and extra information that processes that execute Source or Sink may be compromised. The constraint generated for such an assertion as described in §3.3.3 is

$$\text{lab}_{\text{Source}} \not\subseteq \left(\text{lab}_{\text{Sink}} - \bigcup_{Q \in \text{DistAnc}} \text{creates}_Q \right) \cup \bigcup_{D \notin \text{Declass}} \text{neg}_D$$

Suppose that processes executing Source may be compromised. If a compromised source process wishes to violate the assertion, then it will attempt to send information with the smallest possible label by removing all tags allowed by its negative capability. To constrain that such a process still cannot leak data, the instrumenter alters the left-hand side of the inequality:

$$(\text{lab}_{\text{Source}} - \text{neg}_{\text{Source}}) \not\subseteq \left(\text{lab}_{\text{Sink}} - \bigcup_{Q \in \text{DistAnc}} \text{creates}_Q \right) \cup \bigcup_{D \notin \text{Declass}} \text{neg}_D$$

Suppose that processes executing Sink may be compromised. If a compromised sink process wishes to violate the assertion, then it will attempt to receive information with the largest possible label by adding all tags allowed by its positive capability. To constrain that such a process still cannot leak data, the instrumenter alters the term in the right-hand side of the inequality related to the labels of Sink processes:

$$\text{lab}_{\text{Source}} \not\subseteq \left((\text{lab}_{\text{Sink}} \cup \text{pos}_{\text{Sink}}) \cup \bigcup_{Q \in \text{DistAnc}} \text{creates}_Q \right) \cup \bigcup_{D \notin \text{Declass}} \text{neg}_D$$

We can rewrite the constraints of §2.2 to obtain the full constraint system that allows the instrumenter to isolate Workers even if they are compromised.

D. Integrity

DIFC operating systems give application programmers the power to ensure the secrecy and integrity of their data. The techniques presented in the body of the paper automatically instrument programs to uphold secrecy guarantees. However, they can be extended to also instrument programs to uphold integrity guarantees. The full Flume operating system maps every process to an integrity label, in addition to the process's secrecy label, positive capability, and negative capability. To instrument programs to enforce integrity guarantees, the instrumenter generates for every template variable X an additional constraint variable int_X . The instrumenter generates constraints that assert the Flume semantics for integrity. These are directly analogous to those that assert the semantics of secrecy labels. The function $\text{StepCtrs}(X, Y)$ defined in §3.3.2 is extended to generate the following constraints:

$$\begin{aligned} \text{int}_Y &\subseteq \text{int}_X \cup \text{pos}_Y \\ \text{int}_Y &\supseteq \text{int}_X - \text{neg}_Y \end{aligned}$$

To extend the instrumenter to enforce integrity policies, we would define flow assertions for integrity. The instrumenter would then generate constraints analogous to those of §3.3.3.

E. Formal Trace Semantics of CSP_{DIFC}

We give a formal semantics of CSP_{DIFC} programs as a denotational semantics that maps each program to the set of all event traces

$$\begin{aligned}
\text{ProcTr}((p, \text{SKIP}), \pi, \lambda, \nu, \mu, \eta) &= \text{STEP}(p, P). \text{SPAWN}(p, p'). \text{ProgTr}(\pi - p, \lambda - p, \nu - p, \mu - p, \eta - p) \\
\text{ProcTr}((p, X), \pi, \lambda, \nu, \mu, \eta) &= \text{STEP}(p, P). \text{SPAWN}(p, p'). \\
&\quad \text{ProgTr}((\pi - p)[p' \mapsto \vec{P}(X)], \lambda[p' = p], \nu[p' = p], \mu[p' = p], \eta[p' = p]) \\
\text{ProcTr}((p, \text{Proc}_1 \square \text{Proc}_2), \pi, \lambda, \nu, \mu, \eta) &= \text{STEP}(p, P). \text{SPAWN}(p, p'). \\
&\quad \text{ProgTr}((\pi - p)[p' \mapsto \text{Proc}_1], \lambda[p' = p], \nu[p' = p], \mu[p' = p], \eta[p' = p]) \\
&\quad \cup \text{ProgTr}((\pi - p)[p' \mapsto \text{Proc}_2], \lambda[p' = p], \nu[p' = p], \mu[p' = p], \eta[p' = p]) \\
\text{ProcTr}((p, \text{Proc}_1 \parallel \text{Proc}_2), \pi, \lambda, \nu, \mu, \eta) &= \text{STEP}(p, P). \text{SPAWN}(p, p'). \text{SPAWN}(p, p''). \\
&\quad \text{ProgTr}((\pi - p)[p' \mapsto \text{Proc}_1, p'' \mapsto \text{Proc}_2], \\
&\quad \lambda[p' = p''] = \nu[p' = p''] = \mu[p' = p''] = \eta[p' = p'']) \\
\text{ProcTr}((p, !q \rightarrow \text{Proc}_1), \pi, \lambda, \nu, \mu, \eta) &= \text{STEP}(p, P). \text{SPAWN}(p, p'). (\text{if}(\lambda(p) \subseteq \lambda(q)) \text{ then } (p!q) \text{ else } []). \\
&\quad \text{ProgTr}((\pi - p)[p' \mapsto \text{Proc}_1], \lambda[p' = p], \nu[p' = p], \mu[p' = p], \eta[p' = p]) \\
\text{ProcTr}((p, ?q \rightarrow \text{Proc}_1), \pi, \lambda, \nu, \mu, \eta) &= \text{STEP}(p, P). \text{SPAWN}(p, p'). (\text{if}(\lambda(q) \subseteq \lambda(p)) \text{ then } (p?q) \text{ else } []). \\
&\quad \text{ProgTr}((\pi - p)[p' \mapsto \text{Proc}_1], \lambda[p' = p], \nu[p' = p], \mu[p' = p], \eta[p' = p]) \\
\text{ProcTr}(p, \text{ChangeLabel}(L, M, N) \rightarrow \text{Proc}_1), \pi, \lambda, \nu, \mu, \eta) &= \text{if}(\eta(p)(L) \subseteq \lambda(p) \cup \nu(p) \wedge (\eta(p)(L) \supseteq \lambda(p) - \mu(p)) \\
&\quad \wedge (\eta(p)(M) \subseteq \nu(p)) \wedge (\eta(p)(N) \subseteq \mu(p)) \\
&\quad \text{then } \text{ProgTr}((\pi - p)[p' \mapsto \text{Proc}_1], \\
&\quad \lambda[p' \mapsto L], \nu[p' \mapsto \eta(p)(M)], \mu[p' \mapsto \eta(p)(N)], \eta[p' = p]) \\
&\quad \text{else } \text{ProgTr}((\pi - p)[p' \mapsto \text{Proc}_1], \\
&\quad \lambda[p' = p], \nu[p' = p], \mu[p' = p], \eta[p' = p]) \\
\text{ProcTr}((p, \text{CREATE}_\tau \rightarrow \text{Proc}_1), \lambda, \nu, \mu, \eta) &= \text{ProgTr}((\pi - p)[p' \mapsto \text{Proc}_1], \\
&\quad \lambda[p' = p], (\nu - p)[p' \mapsto \nu(i) \cup \{t\}], (\mu - p)[p' \mapsto \nu(p) \cup \{t\}], \\
&\quad (\eta - p)[p' \mapsto \eta(p)[\tau \mapsto t]], t \text{ fresh}) \\
\text{ProgTr}(\pi, \lambda, \nu, \mu, \eta) &= \bigcup_{(p \mapsto P) \in \pi} \text{ProcTr}((p, P), \pi, \lambda, \nu, \mu, \eta)
\end{aligned}$$

Figure 5. Trace semantics of CSP_{DIFC} .

that it may generate. Two functions define the trace semantics of a program \vec{P} . The function ProgTr defines the set of all traces that program \vec{P} may generate from a given program state, and is defined as follows: first, let T be an infinite set of identifiers for tags created during execution of \vec{P} , and let $\mathcal{L} = 2^T$ be the set of all labels. Let I be the infinite set processes that may be spawned during execution. The function ProgTr takes the following partial functions as input:

1. A program state $\pi : I \rightarrow \text{Proc}$, which maps each process to the template that it executes in its next step.
2. A label map $\lambda : I \rightarrow \mathcal{L}$, which maps each process to its current label.
3. A positive-capability map $\nu : I \rightarrow \mathcal{L}$, which maps each process to its positive capability.
4. A negative-capability map $\mu : I \rightarrow \mathcal{L}$, which maps each process to its negative capability.
5. A tag-namespace map $\eta : I \rightarrow (A \rightarrow T)$, where A is the set of all tag identifiers in \vec{P} . η maps each process to a set of bindings from tag identifiers to tags. In other words, η maps each process to its namespace of tags.

Intuitively, ProgTr defines the set of traces generated by \vec{P} from a state π to be the collection of traces generated from executing the next step of each process in π . By applying ProgTr to a pro-

gram state that binds a single process to the root process template, we obtain the set of all traces that can be generated by the program. Formally, this set is expressed as $\text{ProgTr}(\emptyset[p \mapsto R], \emptyset[p \mapsto \emptyset], \emptyset[p \mapsto \emptyset], \emptyset[p \mapsto \emptyset], \emptyset[p \mapsto \emptyset])$.

ProgTr is co-defined by a second function, ProcTr , which defines how executing one step of a process contributes to the execution trace of a program. Like ProgTr , the function ProcTr takes as input a program state, label map, positive-capability map, negative-capability map, and map to tag namespaces. In addition, ProcTr takes as its first argument a pair (p, P) . Here, p is the identifier of the next process to take a step of execution, and P is the process template that it executes. When process p takes the next step of execution, it adds events to the execution trace and alters the program state based on the form of P . §3.1 informally describes what events are added and how the program state is altered. The equations in Fig. 5 define these effects formally.

The definitions in Fig. 5 make use of the following notation. Let f be an arbitrary map (in Fig. 5, f stands for either λ , ν , or μ). Then $f - i$ denotes f without a binding for domain element i . Map $f[i \mapsto d]$ is the same map as f , with the exception that domain element i is bound to range element d . Map $f[p' = p]$ is the same map as f , but with $f[p' = p](p') = f(p)$ and $f(p)$ undefined. Each equation has an implicit condition that any process spawned in the step of execution, such as p' or p'' , is not previously in the domain

of any map. The operator \cdot is used to prefix a single event to a set of traces. If E is an event and T is a set of traces, then $E \cdot T$ denotes the set of traces $\{E \cdot T \mid T \in T\}$, where $E \cdot T$ denotes prefixing the event E to the trace T .

F. Proof of Correctness

We now prove that when the instrumenter produces a program, the program is correct according to the definition of §3.2. We first give definitions and lemmas that allow us to prove this.

Def. 4. *Each trace of a program corresponds one-to-one with an execution of a program. An execution is a sequence of spawns $p \rightarrow q$, where p, q are processes, and $p \rightarrow q$ denotes that p executes the template to which it is bound, and then spawns q .*

Let $\sigma = p_0 \rightarrow p_1, \dots, p_{n-1} \rightarrow p_n$ be an execution of program \vec{P} , and let \vec{P}' be an instrumentation of \vec{P} using the procedure given in §3. σ has a corresponding execution $\text{inst}_{\vec{P}'}(\sigma)$ in \vec{P}' , which is the same with the exception of label manipulations, constructed as follows. Note that the instrumenter rewrites every equation $X = T$ in \vec{P} , to a set of equations of the form $X = E_0 \rightarrow X_0; X_0 = E_1 \rightarrow X_1; \dots; X_n = T$, where each E_i is either a CREATE or ChangeLabel event. To construct $\text{inst}_{\vec{P}'}$, rewrite σ by replacing each spawn $p \rightarrow q$ with $p \in T$ with a sequence of spawns $p \rightarrow p_0, p_1 \rightarrow p_2, \dots, p_{n-1} \rightarrow p_n$, where each $p_i \in \vec{P}'(X_i)$. The resulting execution is in \vec{P}' .

Furthermore, for every execution in \vec{P}' , there is a corresponding execution $\text{erase}(\sigma)$ in \vec{P} that is the same, with the exception that it does not manipulate labels. Let $p \rightarrow q, q \rightarrow r$ be a pair of spawns in σ , an execution of \vec{P}' . If $q \in \vec{P}'(X)$ for some template X that is in \vec{P}' but not in \vec{P} , then replace the pair of spawns with a single spawn $p \rightarrow r$. After repeating this for all such q , the resulting execution, $\text{erase}(\sigma)$ is an execution of \vec{P} .

We now establish that if \vec{P}' is instrumented to respect an protection assertion, then all flows described in the assertion are successful in all executions of \vec{P}' .

Lem. 1. *Let \vec{P} be instrumented according to §3 for a policy $\mathcal{F} = (S, \mathcal{R})$ to form \vec{P}' . Let σ be an execution of \vec{P}' such if $p \rightarrow p'$ and $q \rightarrow q'$ are in σ where p executes template $!q \rightarrow X$ and q executes template $?p \rightarrow Y$, then there is some template $\text{Prot}(\text{Source}, \text{Sink}, \text{Anc}) \in \mathcal{R}$ such that $p \in \text{Source}$, $q \in \text{Sink}$, and p and q share their most recent ancestor in Anc. Then $(p!q), (q?p) \in \text{Tr}(\sigma)$.*

Proof. Let t be a tag where $t \in \text{lab}_P$, and t is bound to an identifier τ . By the constraint of §3.3.3, $\text{lab}_{\text{Source}} \subseteq \text{lab}_{\text{Sink}}$, so τ is bound a tag u in the label of q . Furthermore, τ was bound at a template in $\text{Const}_{\text{Anc}}$. p and q share their most recent ancestor in Anc, so by the definition of $\text{Const}_{\text{Anc}}$, their tags bound to τ are the same. Thus $t = u$, and in general, the label of p is a subset of the label of q . Thus when p attempts to send data to q , the send is successful, and the program generates the event $p!q$. Symmetrically, when q attempts to receive data from p , it is successful, and the program generates the event $q?p$. \square

An immediate corollary of Lem. 1 is that for an execution σ of \vec{P} , $\text{Tr}(\sigma) = \text{Tr}(\text{inst}(\sigma))$. Furthermore, if σ is a trace of \vec{P}' , then $\text{Tr}(\sigma) = \text{Tr}(\text{erase}(\sigma))$. We are now ready to prove that programs produced by the instrumenter are correct.

Thm. 4. *Given program \vec{P} and policy $\mathcal{F} = (S, \mathcal{R})$, let \vec{P}' be the program produced by the instrumenter according to §3. Then $\vec{P}' \models (\vec{P}, \mathcal{F})$.*

Proof. To prove that $\vec{P}' \models (\vec{P}, \mathcal{F})$, we must prove that \vec{P}' satisfies each of the instrumentation conditions given in §3.2.2. We first show that \vec{P}' satisfies the secrecy instrumentation condition by considering an arbitrary secrecy flow assertion $\text{Secrecy}(\text{Source}, \text{Sink}, \text{Declass}, \text{Anc}) \in \mathcal{S}$. Consider an arbitrary execution of \vec{P} with a sequence of sends and receives $p \rightarrow p_1 \rightarrow \dots \rightarrow p_n = q$, where $p \in \text{Source}$, $q \in \text{Sink}$, $p_i \notin \text{Declass}$, and $p_i \rightarrow p_j$ denotes that p_i sends information to p_{i+1} and p_{i+1} later receives the information. We need only consider the case where p and q do not share an ancestor in Anc. By the constraints of §3.3.3 and the instrumentation of §3.5, the label of p contains a tag t created in some template P such that no tag created at P is in the negative capability of any process not in Declass. Thus if a p_i received data from p_{i-1} , it had to have t in its label, and could not remove t from its label. By §3.3.3, either (1) no tag created at P is in the label of q or (2) P is distinct in Anc. If (1) holds, then the label of q does not contain t , and thus cannot contain the label of p_{n-1} . Thus p_{n-1} cannot send information to q . If (2) holds, then while the label of q may contain a tag u created at P , tags t and u must be distinct. Thus p_{n-1} still fails to send information to q . The secrecy flow assertion is thus respected, and in general $\vec{P}' \models_S \mathcal{S}$.

We now show that \vec{P}' satisfies the transparency conditions induced by \mathcal{R} . Let $T \in \text{Tr}(\vec{P})$ generated by execution σ . By Lem. 1, the trace of $\text{inst}(\sigma)$ is identical, and thus $T \in \text{Tr}(\vec{P}')$, and $\vec{P}' \models_T (\vec{P}, \mathcal{F})$. To see that \vec{P}' satisfies the containment condition induced by, consider a trace $T \in \text{Tr}(\vec{P}')$ generated by an execution σ for which all flows are protected by \mathcal{R} . By Lem. 1, $\text{erase}(\sigma)$ has the same trace as σ . Thus $T \in \text{Tr}(\vec{P})$, and $\vec{P}' \models_C (\vec{P}, \mathcal{F})$. \square

G. Proof of Hardness of Constraint Solving

Let a DIFC synthesis constraint problem C be a constraint produced from the following grammar for Ctr:

$$\begin{aligned} \text{Ctr} &:= \text{Ctr} \wedge \text{Ctr} \\ &|\text{VAR} \subseteq \text{SetExpr} \\ &|\text{VAR} \not\subseteq \text{SetExpr} \\ \text{SetExpr} &:= \emptyset \\ &|\text{VAR} \\ &|\text{SetExpr} \cup \text{SetExpr} \\ &|\text{SetExpr} - \text{SetExpr} \end{aligned}$$

Let V_C be the set of all variables appearing in C . A solution to C is some powerset lattice and a mapping from each variable in the constraint system to an element in the lattice that satisfies the constraints. Formally, it is a pair (n, s) where $n \in \mathbb{N}$ and $s : V_C \rightarrow 2^n$ is such that $s(c) = \mathbf{T}$, where $s(c)$ is the evaluation of variables lifted to an evaluation of expressions and constraints in the standard way. Let DIFC-SAT be the problem of deciding whether any DIFC synthesis constraint problem has a solution. Observe that while the grammar for Ctr does not describe all constraints that may be generated to instrument a DIFC program, for any constraint it is straightforward to construct a program and policy and induce the constraint.

Thm. 5. *DIFC-SAT is NP-complete.*

Proof. We first show that the problem is in NP. Observe that Thm. 3 applies to the constraints in DIFC-SAT. Thus by Thm. 3, if C has a solution, then it has one defined in a number of tags B that is linear in the number of constraints. For a non-deterministic

machine to solve an instance C in polynomial time, it guesses a solution (m, s) where m is bounded linearly by $|C|$. The assignment s can then be validated in a number of steps polynomial in the size of $|C|$.

We show completeness by reduction from 3SAT, first giving the reduction and then proving its correctness. Let φ be an instance of 3SAT. Assume without loss of generality that φ is in CNF. Generate a set variable $X_{\mathbf{T}}$ and constrain it with $X_{\mathbf{T}} \not\subseteq \emptyset$. For every propositional variable $x \in \text{Vars}(\varphi)$, generate two set variables X, \bar{X} and constrain them to be disjoint, expressed in the grammar of constraints as $X \subseteq X - \bar{X}$. Let $l_1 \vee l_2 \vee l_3$ be a disjunctive clause in φ . If $l_i = x_i$ is a variable, then let L_i be the corresponding set variable X_i , and if $l_i = \neg x_i$ is the negation of a variable, let L_i be \bar{X}_i . Construct the set constraint $X_{\mathbf{T}} \subseteq L_1 \cup L_2 \cup L_3$. Let $D(\varphi)$ be the collection of all constraints generated. $D(\varphi)$ has a DIFC solution if and only if φ has a solution.

We now prove that the given reduction is correct. First, suppose that the 3SAT instance φ has a satisfying assignment s . Then $D(\varphi)$ has a solution $(2^1, s')$ where $s'(X) = \{0\}$ if and only if $s(x) = \mathbf{T}$. To see this, observe that under s' each pair X, \bar{X} is disjoint, trivially. Furthermore, it must be the case that $X_{\mathbf{T}} = \{0\}$, and each expression $X_{\mathbf{T}} \subseteq L_1 \cup L_2 \cup L_3$ corresponding to some clause $l_1 \vee l_2 \vee l_3$ must consist of at least one set variable that contains 0.

Now suppose that $D(\varphi)$ has a solution (n, s') . For a propositional literal x , let $x = \mathbf{T}$ if and only if $k \in s'(X_{\mathbf{T}})$ for any fixed $k < n$. For this to be a valid solution to φ , it must be the case that no propositional variable and its complement are both assigned to \mathbf{T} . However, this cannot be the case, as every pair of variables X, \bar{X} is constrained to be non-overlapping, so it cannot be the case that both would contain any fixed element k . Additionally, it must be the case that for every $l_1 \vee l_2 \vee l_3$, in the corresponding set expression $L_1 \cup L_2 \cup L_3$, at least one set variable contains k . This follows because the set expression is constrained as $X_{\mathbf{T}} \subseteq L_1 \cup L_2 \cup L_3$, and $k \in X_{\mathbf{T}}$. \square

H. Persistent Principals

Programs may sometimes need to be instrumented to have information flow to and from *persistent principals*, such as files, which may have labels and capabilities defined before the system is executed. To handle such cases, we extend the DIFC instrumenter beyond the techniques given in §3, which only reason about principals such as processes that are introduced during execution. Persistent principals are handled in an analogous way: the possibly unbounded set of labels that they may have are abstracted to a finite set tag identifiers, the requirements for how their information may flow are encoded as constraints over these identifiers, and the resulting constraint system is fed to a constraint solver. The key difference is that while in §3 each abstract tag represented all tags created when executing a template, with persistent principals each abstract tag represents all tags that a principal may have before execution.

Formally, we extend the set of flow assertions defined in §3.2 to include four additional assertions:

- **WriteAtMost**(user, Proc) specifies that any process executing template Proc should only be able to write to files to which the persistent principal user is able to write.
- **WriteAtLeast**(user, Proc) specifies that if a user is able to write to a file, then a process executing Proc should be able to write to it as well.
- **ReadAtMost**(user, Proc) specifies that any process executing template Proc should only be able to read from files from which the persistent principal user is able to read.

- **ReadAtLeast**(user, Proc) specifies that if a user is able to from a file, then a process executing Proc should be able read from it as well.

We extend the constraint system of §3.3 to reason about such flow assertions. For every template variable X in a CSP_{DIFC} program \vec{P} , we generate additional constraints variables **PersLab**, **PersPos**, and **PersNeg**, which are used to store the labels, positive capabilities, and negative capabilities associated with persistent principals. For each persistent principal u in the set U of all persistent principals mentioned in the set of flow assertions, we generate fresh abstract tags l_u, p_u , and n_u . The represent, respectively, the set of all tags in the label, positive capability, and negative capability of u when the programs executes.

The constraints generated over persistent constraint variables are analogous to those for non-persistent principals, with one key difference. Recall from §3.3.1 that for a template X , the variables $\text{lab}_X, \text{pos}_X$, and neg_X share one domain of elements, and that the value of capability variables pos_X and neg_X constrains how the values in the label variable lab_X may change. This is feasible for processes, because all abstract tags in the values of these variables correspond to tags created during execution of the program. However, when dealing with persistent principals, the instrumenter cannot assume any relationship between the tags in the label of a user u and those in its positive or negative capabilities. Thus the instrumenter must assume that once the tags in a user's label have been added to the label of a process, those tags cannot be removed. Formally, for every pair of template variables X, Y given to **StepCtrs** as defined in §3.3.2, the instrumenter generates the constraint $\text{PersLab}_X \subseteq \text{PersLab}_Y$. No semantic constraints restrict how a process may add or remove persistent capabilities, as we assume that a process may add the capabilities of any user, and may always drop capabilities.

The instrumenter generates constraints to encode each flow assertion that appears in a policy. It generates constraints per assertion as follows. In each case, let l_u, p_u , and n_u be the abstract tags allocated for the label, positive capability, and negative capability of user u .

- **WriteAtMost**(user, P): the instrumenter must constrain that the label of P contains the label of user, and that the negative capabilities of P are contained by the negative capabilities of user. Formally:

$$\begin{aligned} \text{PersLab}_P &\supseteq \{l_u\} \\ \text{PersPos}_P &\subseteq \{p_u\} \end{aligned}$$

- **WriteAtLeast**(user, P): the instrumenter must constrain that the label of P is contained by the label of user, and that the negative capabilities of P contain the negative capabilities of the user. Formally:

$$\begin{aligned} \text{PersLab}_P &\subseteq \{l_u\} \\ \text{PersNeg}_P &\supseteq \{n_u\} \end{aligned}$$

- **ReadAtMost**(user, P): the instrumenter must constrain that the label of P is contained by the label of user, and that the positive capabilities of P are contained by the negative capabilities of user. Formally:

$$\begin{aligned} \text{PersLab}_P &\subseteq \{l_u\} \\ \text{PersPos}_P &\subseteq \{p_u\} \end{aligned}$$

- **ReadAtLeast**(user, P): the instrumenter must constrain that the label of P contains the label of user, and that the positive capabilities of P contain the positive capabilities of user. For-

```

Prog ::= stmt
stmt ::= VAR := exp
      | send(p)
      | recv(p)
      | spawn(P)
      | stmt1; stmt2
      | if exp then stmt1 else stmt2
      | while (exp) stmt1

```

Figure 6. IMP: a simple imperative language.

mally:

$$\text{PersLab}_P \supseteq \{l_u\}$$

$$\text{PersPos}_P \supseteq \{p_u\}$$

The instrumenter generates code based on a constraint solution as follows. The instrumenter represents a persistent principals as a quadruple of four objects:

1. A program variable that contains a *user token*.
2. A *label accessor function* that takes as input the user token and returns the persistent label of the persistent principal.
3. A *positive capability accessor function* that takes as input the user token and returns the positive capability of the persistent principal.
4. A *negative capability accessor function* that takes as input the user token and returns the negative capability of the persistent principal.

Flume provides such accessor functions for users in its API. Other accessors may be defined and given to the instrumenter as needed.

Persistent principals are used to instrument the FlumeWiki server described in §4. FlumeWiki requires that a Worker process execute with exactly the ability to write to and read from exactly the same files as the user who issues the request. This requirement is expressed as a conjunction of the flow assertions `WriteAtMost(user, Worker)`, `WriteAtLeast(user, Worker)`, `ReadAtMost(user, Worker)`, and `ReadAtLeast(user, Worker)`.

I. Instrumenting Imperative Programs

§3 considers how to instrument CSP_{DIFC} programs. In practice, this is not a limitation, because imperative programs can be modeled as CSP_{DIFC} programs, and an instrumented CSP_{DIFC} program can be translated to an instrumented imperative program.

I.1 From Uninstrumented Imperative Programs to Uninstrumented CSP Programs

Imperative programs can be automatically modeled as CSP_{DIFC} programs. To demonstrate this, we present a toy imperative language, IMP, whose syntax is given in Fig. 6. An IMP program is an imperative statement, which may optionally have a control-flow label. Statements can be constructed as normal assignments, the distinguished statements `send`, `recv`, and `spawn`, and the standard control-flow structures `if` and `while`. Calls to `send` and `recv` communicate information to a process identified as p , while `spawn(Q)` launches a new process starting execution at the statement labeled Q , while continuing execution of the current process. The set of expressions that can occur in an IMP program is ignored by the instrumenter, and thus we omit a detailed description. The semantics of IMP programs are standard.

An IMP program P can be translated into a program in CSP_{DIFC} that over-approximates the set of executions of P . Such a translation is described by the rules in Fig. 7, which are written in an ML-like syntax. In Fig. 7, the function `ImpToCSP` takes as input an IMP program to be translated and an auxiliary CSP_{DIFC} process variable P . Variable P is assumed to be bound by a process equation to a CSP_{DIFC} process that models all execution after the IMP program. The output of `ImpToCSP` is a list of equations defining all process templates. Each rule that partially defines `ImpToCSP` introduces a fresh process variable X . The variable X binds to the CSP_{DIFC} process representation of the IMP statement being translated.

The translation `ImpToCSP` yields a CSP program whose trace semantics “reasonably” over-approximates the set of traces of the original IMP program in the following sense. If one were to replace all predicates in all guards in the original IMP program with a predicate whose value was non-deterministic, then the set of traces of the IMP program and CSP_{DIFC} process obtained by translation would be identical.

I.2 Instrumented CSP_{DIFC} Programs to Instrumented Imperative Programs

If we translate imperative programs to CSP_{DIFC} programs using the rules of Fig. 7, then every imperative program statement corresponds to exactly one CSP_{DIFC} template variable. Suppose that the instrumenter produces an instrumented CSP_{DIFC} program that satisfies a policy. The instrumented program contains templates of two new forms that must be translated to a corresponding form in the imperative program. First, a CSP_{DIFC} program may contain a template of the form `CREATEt → P`. To translate such a template to an imperative program, the instrumenter declares in the imperative program a global tag variable `tag_t` and instruments the corresponding location with a call `t := create_tag()`.

Second, a CSP_{DIFC} program may contain a template of the form `ChangeLabel(L, M, N) → P`, where L, M, N are sets of tag identifiers. The translator prepends the corresponding program location with calls to the Flume API function that set the label values of the process to the corresponding label values. Let $t \in L$ be a tag identifier. The instrumenter generates code that uses an empty scratch variable (e.g. `tmp`), and adds to it the tag stored in the corresponding label variable `tag.t`.

Exa. 12. Suppose that $x := \text{exp}$ is a statement in an imperative program given to the DIFC instrumenter, and that the instrumenter translates the statement to a CSP_{DIFC} program in which the statement is modeled by an equation $X = P$. Suppose that the instrumenter finds a solution for the program containing the statement and a policy, and rewrites the CSP_{DIFC} equation to be $X = \text{ChangeLabel}(L, M, N) \rightarrow P$, where $L = \{t\}$, $M = \{t, u\}$, and $N = \emptyset$. The instrumenter then generates the following imperative code:

```

clear_tag_set(tmp);
expand_tag_set(tmp, tag.t);
set_label(tmp);
clear_tag_set(tmp);
expand_tag_set(tmp, tag.t);
expand_tag_set(tmp, tag.u);
set_pos_cap(tmp);
clear_tag_set(tmp);
set_neg_cap(tmp);
x := exp;

```

$$\begin{aligned}
\text{ImpToCSP}(x := \text{exp}, P) &= [\text{STEP}(X) \rightarrow P] \\
\text{ImpToCSP}(\text{send}(c), P) &= [X \text{=! } c \rightarrow P] \\
\text{ImpToCSP}(\text{recv}(c), P) &= [X \text{=? } c \rightarrow P] \\
\text{ImpToCSP}(\text{spawn}(Q), P) &= [X = P \parallel Q] \\
\text{ImpToCSP}(\text{stmt}_1; \text{stmt}_2, P) &= \\
&\quad \text{let}[Y_1 = Z_1; \dots] \text{ as defs} = \text{ImpToCSP}(\text{stmt}_2, P) \text{ in} \\
&\quad \quad [X = \text{ImpToCSP}(\text{stmt}_1, Y_1)] . \text{defs} \\
\text{ImpToCSP}(\text{if exp then stmt}_1 \text{ else stmt}_2, P) &= \\
&\quad \text{let}[Y_1 = Z_1; \dots] \text{ as defs}_1 = \text{ImpToCSP}(\text{stmt}_1, P) \text{ in} \\
&\quad \text{let}[Y_2 = Z_2; \dots] \text{ as defs}_2 = \text{ImpToCSP}(\text{stmt}_2, P) \text{ in} \\
&\quad [X = Y_1 \square Y_2]. \text{defs}_1. \text{defs}_2 \\
\text{ImpToCSP}(\text{while (exp) stmt}, P) &= \\
&\quad \text{let}[Y_1 = Z_1; \dots] \text{ as defs}_1 = \text{ImpToCSP}(\text{stmt}, P) \text{ in} \\
&\quad \quad [X = P \square Y_1] . \text{defs}_1
\end{aligned}$$

Figure 7. Translation rules from IMP statements to CSP_{DIFC} processes.