Implementing Support for Extensible Power Modeling in gem5

Alex Smith, Matthew D. Sinclair University of Wisconsin-Madison adsmith@cs.wisc.edu sinclair@cs.wisc.edu

I. INTRODUCTION

Power consumption has increasingly become a first-class design constraint [1] to satisfy requirements for scientific workloads and other widely used workloads, such as machine learning. To meet performance and power requirements, system designers often use architectural simulators, such as gem5, to model component and system-level behavior. However, performance and power modeling tools are often isolated and do not make it accessible to integrate with one another for rapid performance and power system co-design. Although studies have previously explored power modeling with gem5 and validation on real hardware [2], there are several flaws with this approach. First, power models are sometimes not open source, making it difficult to apply them to different simulated systems. The current interface for implementing power models in gem5 also relies on hard-coded strings provided by the user to model dynamic and static power. This makes defining power models for components cumbersome and restrictive, as gem5's MathExpr string formula parser has support for limited mathematical operations. Third, previous works only implement one form of power model for one component. This unnecessarily limits users from combining other power models, which may model certain system components with higher accuracy. Instead, we posit that decoupling how power models are integrated with simulators from the design of power models themselves will enable better power modeling in simulators. Accordingly, we extend our prior work on designing and implementing an extensible, generalizable power modeling interface [3] by integrating support for McPAT [4] into it and validating it emits correct power values.

II. IMPLEMENTATION AND METHODOLOGY

Our implementation introduces a new SimObject class which accepts a Python function instead of a string equation to model power consumption. This enables users to define a power model with more flexible, customized behavior than the current MathExpr API. For example, MathExpr does not support nesting functions and only supports operations that can be expressed in a string. This interface enables users to integrate any power model which best suits their needs, including pre-existing power models or power models they have created.

Methodology: To demonstrate this flexibility, we implemented McPAT into this interface for the in-order TimingSimple and Minor CPUs and the out-of-order (O3) CPU, each with private L1 data and instruction caches and a shared L2 cache. Next, to



Fig. 1: Power modeling results. Each tick is in the form of (Benchmark, Simulator, CPU Type)

validate our implementation, we selected a handful of accessible benchmarks including Hello World, IAX, SAX, IAXPY, SAXPY, and DAXPY. For each benchmark, we validated their new gem5 power results against those from McPAT for all 3 CPUs. Although these benchmarks have natural regions of interest (ROIs), we model the power for the entire program due to issues with m5ops improperly resetting statistics.

Results: Figure 1 shows our implemented gem5 McPAT support has nearly identical power consumption to standalone McPAT. Although most results follow expected patterns (e.g., SAX consumes less energy than SAXPY), several do not. For example, DAXPY does not always consume more energy than SAXPY, despite the increased precision in DAXPY, because McPAT does not properly model the difference between single and double precision operations. Similarly, some instructions are categorized as vector instructions, which McPAT does not support – causing instances such as IAXPY to report less power than IAX under Timing. Nevertheless, since our integrated support mirrors that of McPAT, these issues are not specific to our integration.

III. CONCLUSION AND FUTURE WORK

To overcome the limitations of gem5's current power modeling API, we implemented a new power modeling interface in gem5. To demonstrate the potential of our approach, we implemented and are releasing this support, which we validated against the standalone tool. Thus, this support makes codesigning early-stage designs for both performance and power more practical for gem5's users, and makes power models accessible to computer architecture researchers. Moving forward, we are extending this support to model power [5], [6] for gem5's GPU models [7]–[10].

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