

Programming and Usage Models for Non-Volatile Memory

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Motivation

- DRAM replacement technologies are non-volatile
 - Resistance rather than capacitance
 - Low power – no refresh!
- Many open questions on how to use non-volatile memory (NVM)

Who we are: Haris

- Ph.D., MS: University of Wisconsin
- Employment: HP Labs

Who we are: Mike

- Associate professor: University of Wisconsin
- Ph.D., MS: University of Washington
- Employment: Microsoft, Windows NT/2000

Tutorial Outline

1.	Introduction	5 minutes
2.	NVM Technologies	15 minutes
3.	Programming Models for NVM	1.5 hours
	Break	15 minutes
4.	Hardware support for NVM programming	45 minutes
5.	Getting started with NVM programming	40 minutes