An Analysis of Persistent Memory Use with WHISPER

Sanketh Nalli, Swapnil Haria, Michael M. Swift, Mark D. Hill, Haris Volos*, Kimberly Keeton*

Persistent Memory = NVM on memory bus

Progress slowed by micro-benchmarks

We contribute:

• Open source WHISPER benchmark suite
  research.cs.wisc.edu/multifacet/whisper

• Analysis of ordering and consistency in WHISPER
WHISPER Analysis

Wisconsin-HP Labs Suite for Persistence, a benchmark suite for Persistent Memory (PM)

- 4% accesses to PM, 96% accesses to DRAM
- 5-50 epochs/tx, primarily from memory allocation & logging
- More in the full talk

Hands Off Persistence System (HOPS) decouples ordering from durability, performs 24% faster than x86-64

Please attend our talk at session 3A today @1:30 pm